

FIG. 1

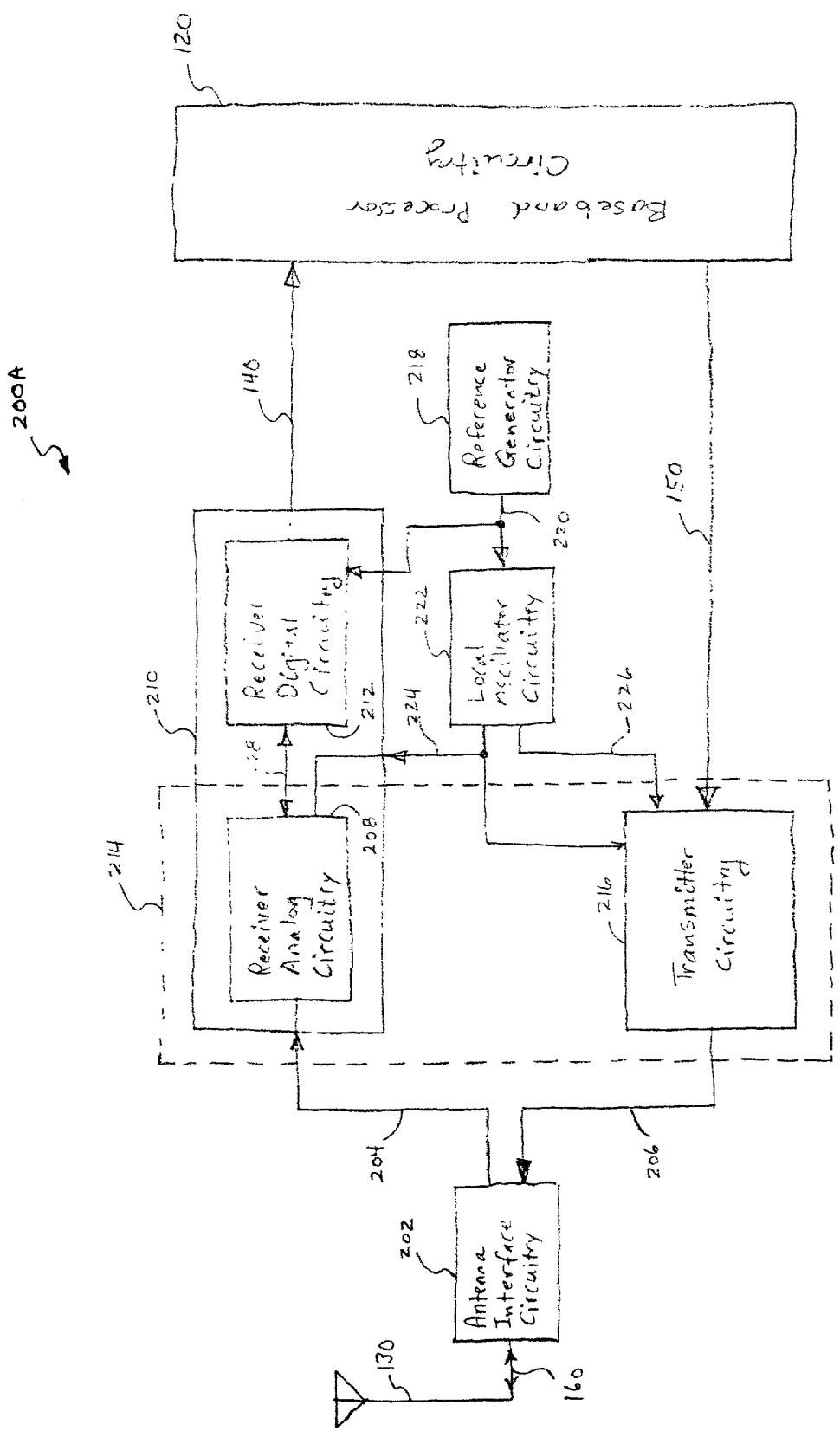
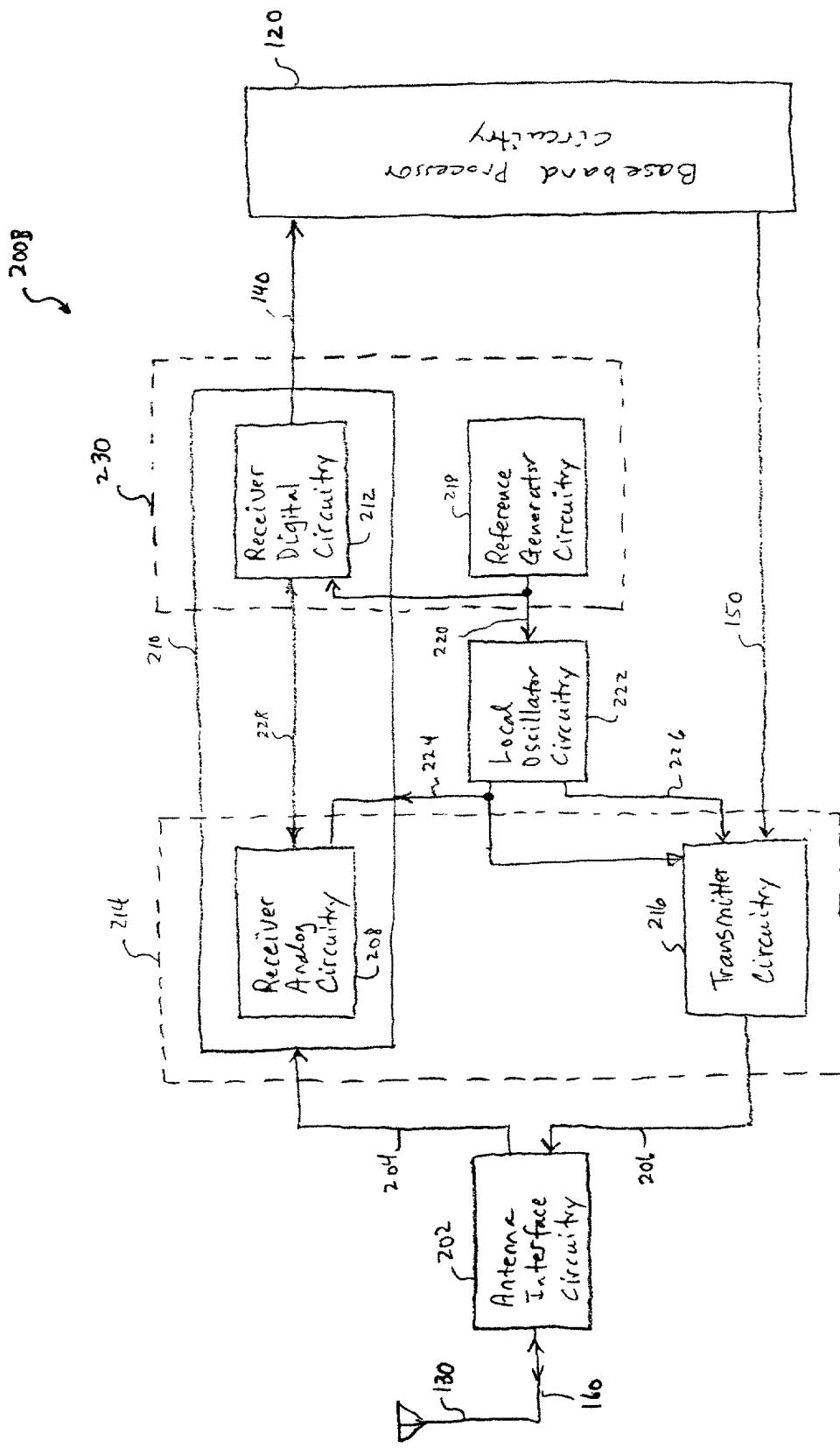


FIG. 2A

FIG. 2B



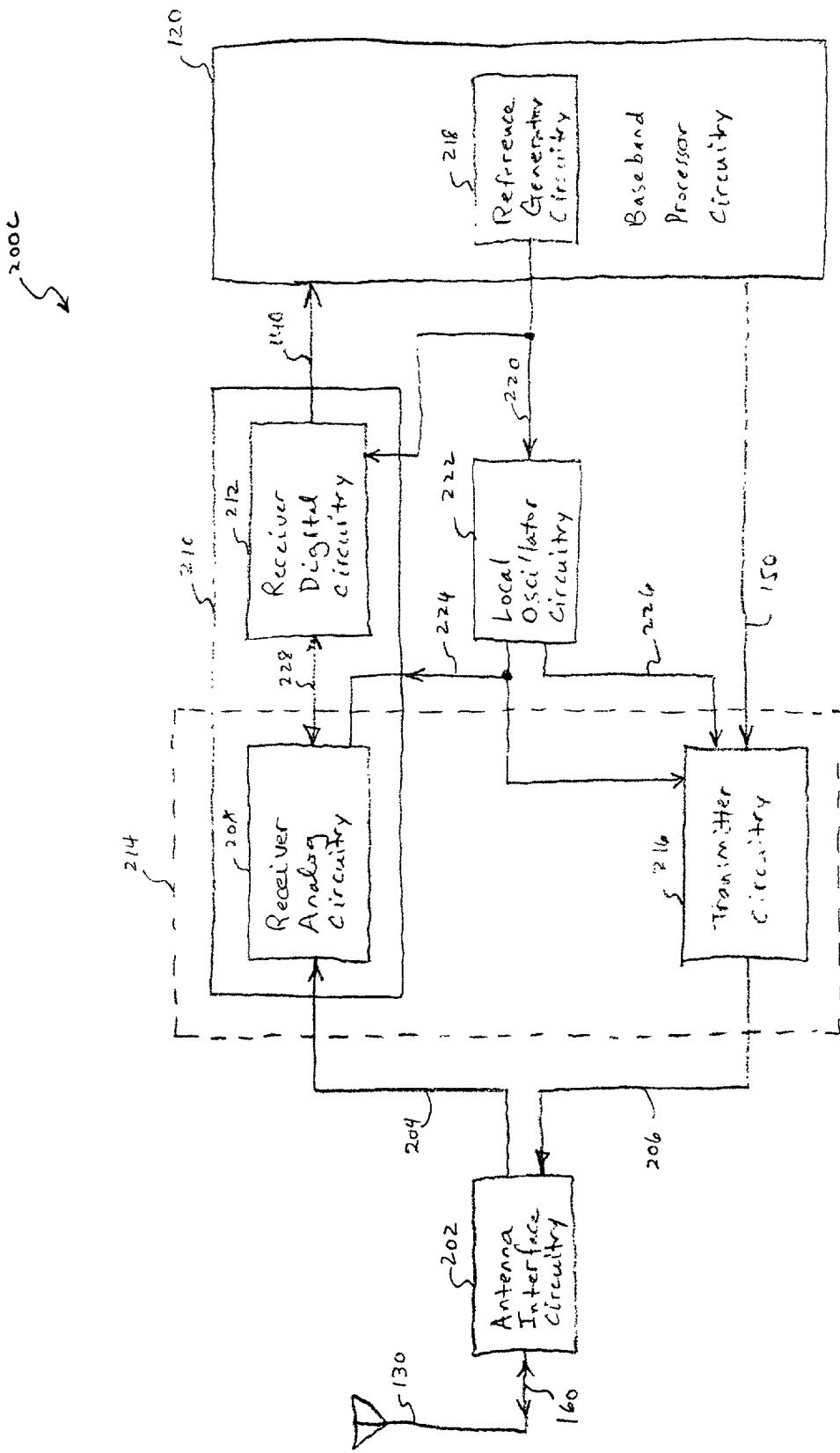
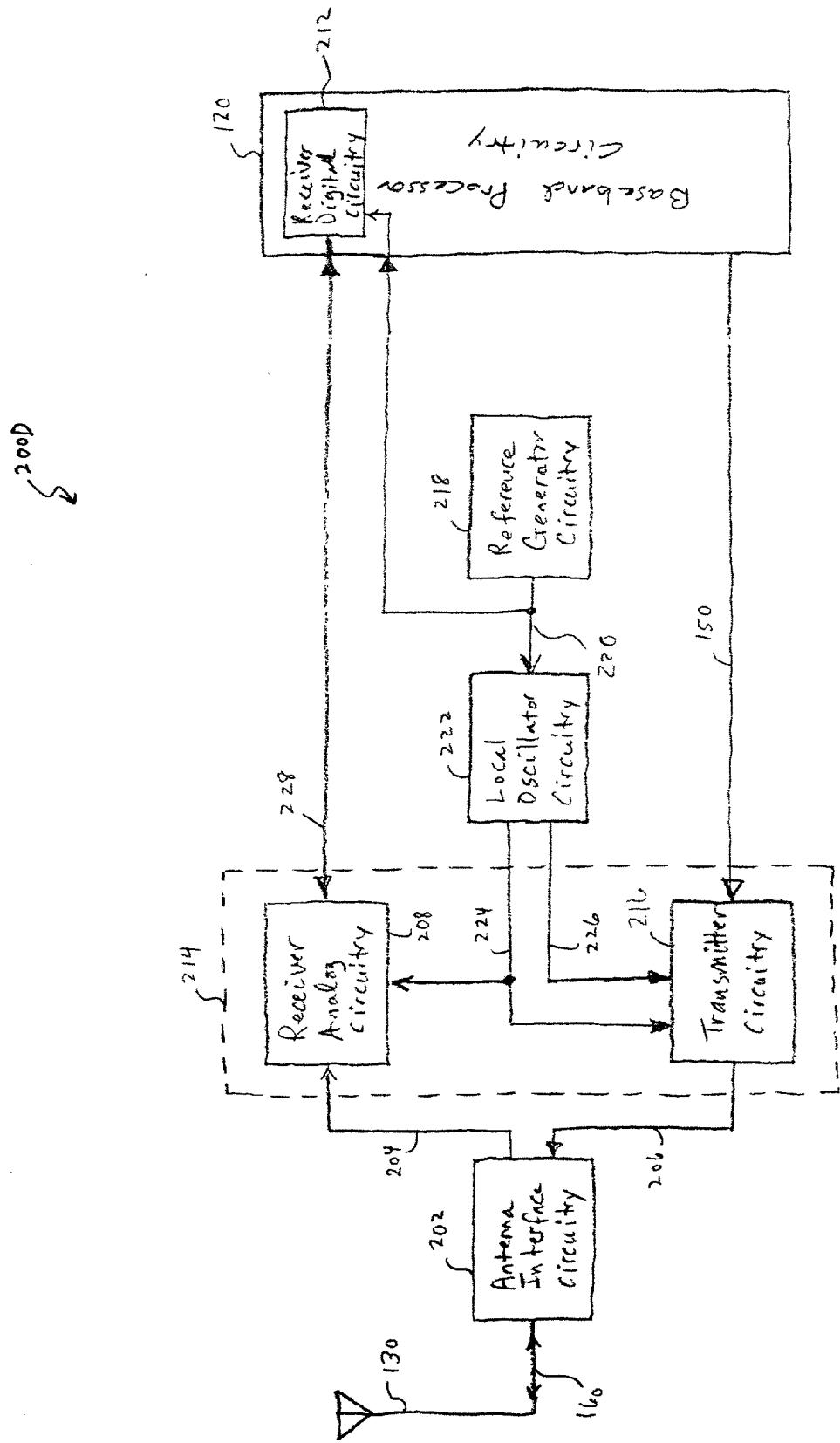


FIG. 2C

FIG. 2D



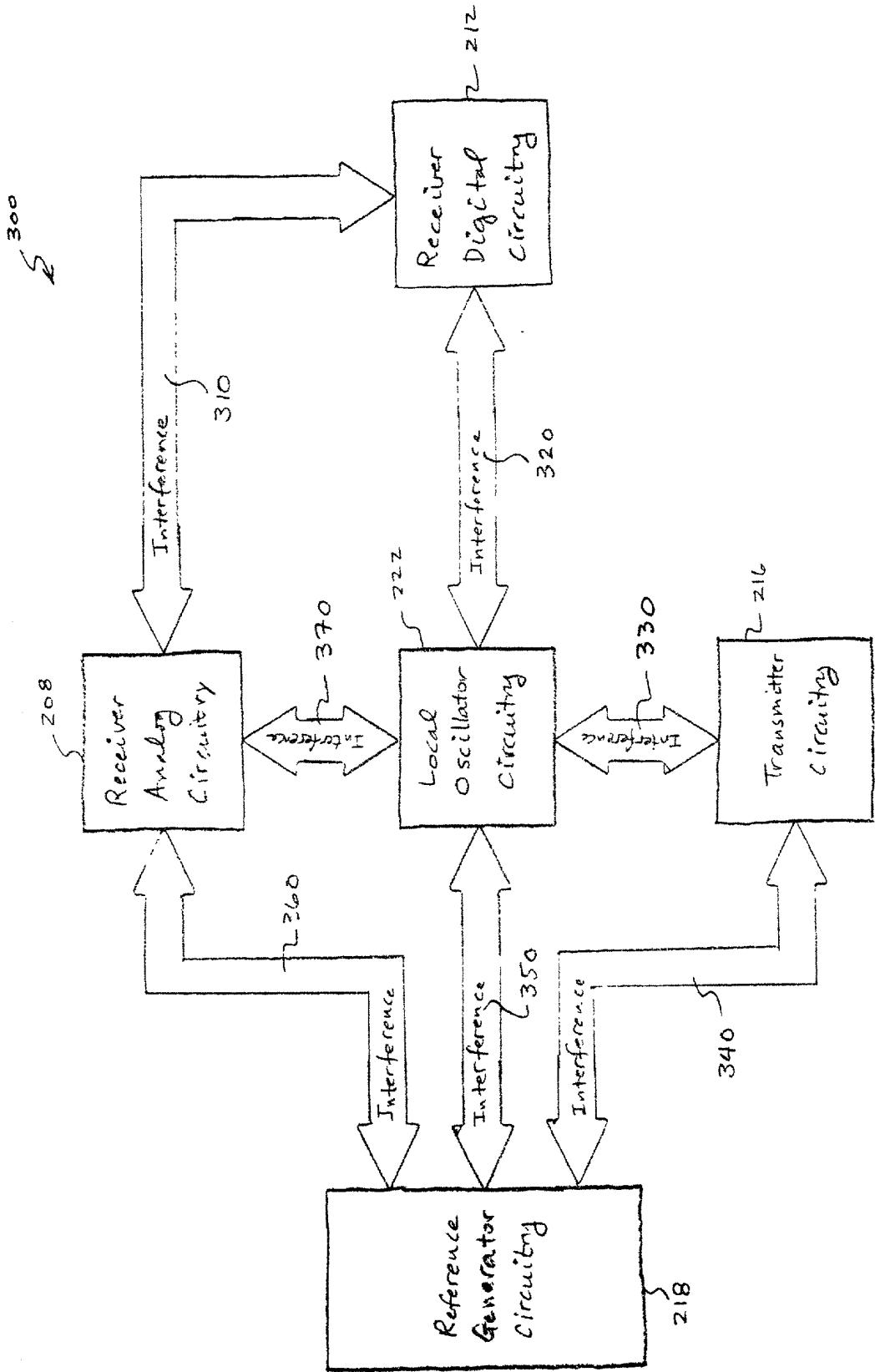


FIG. 3

400

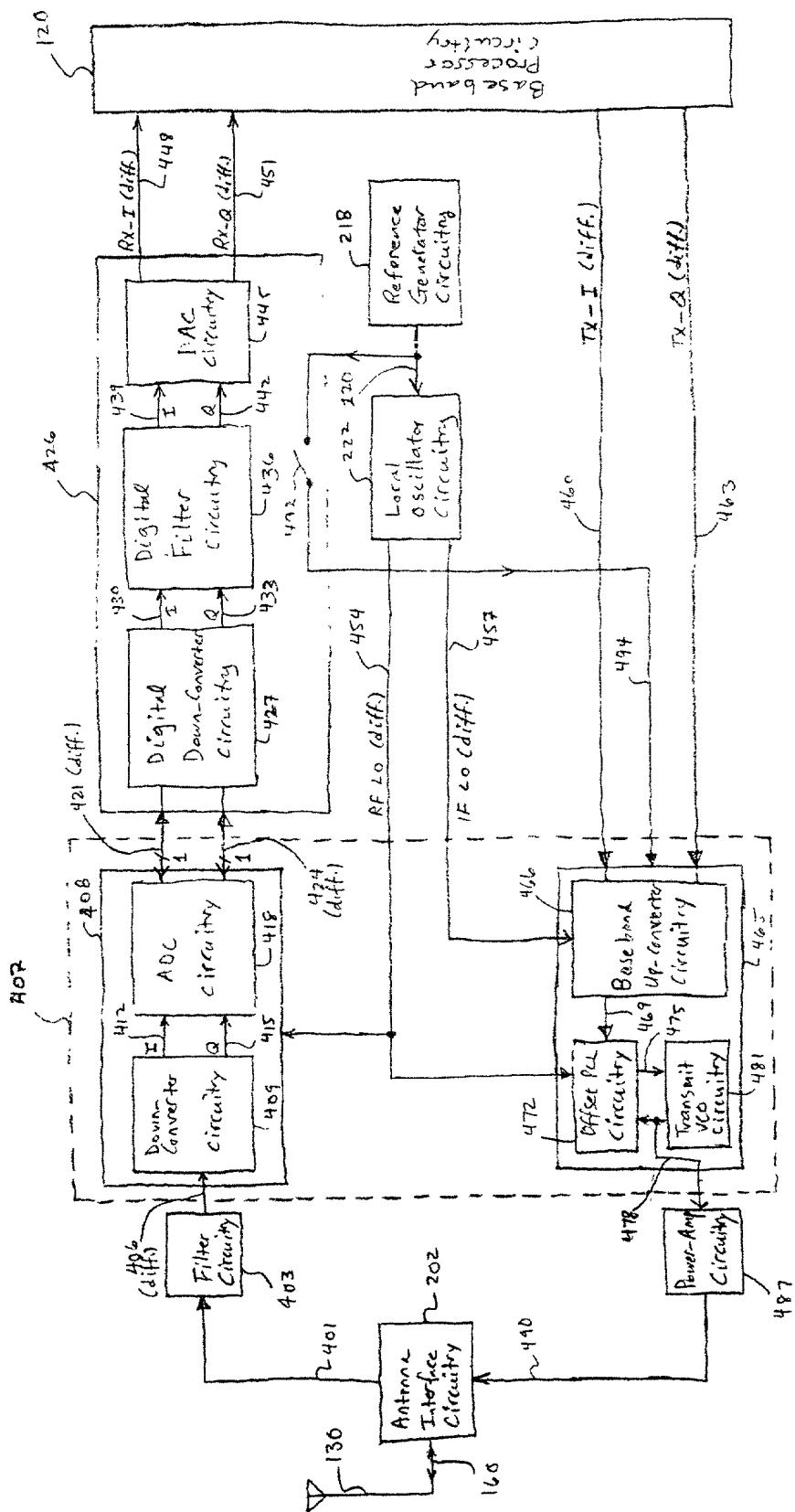


FIG. 4

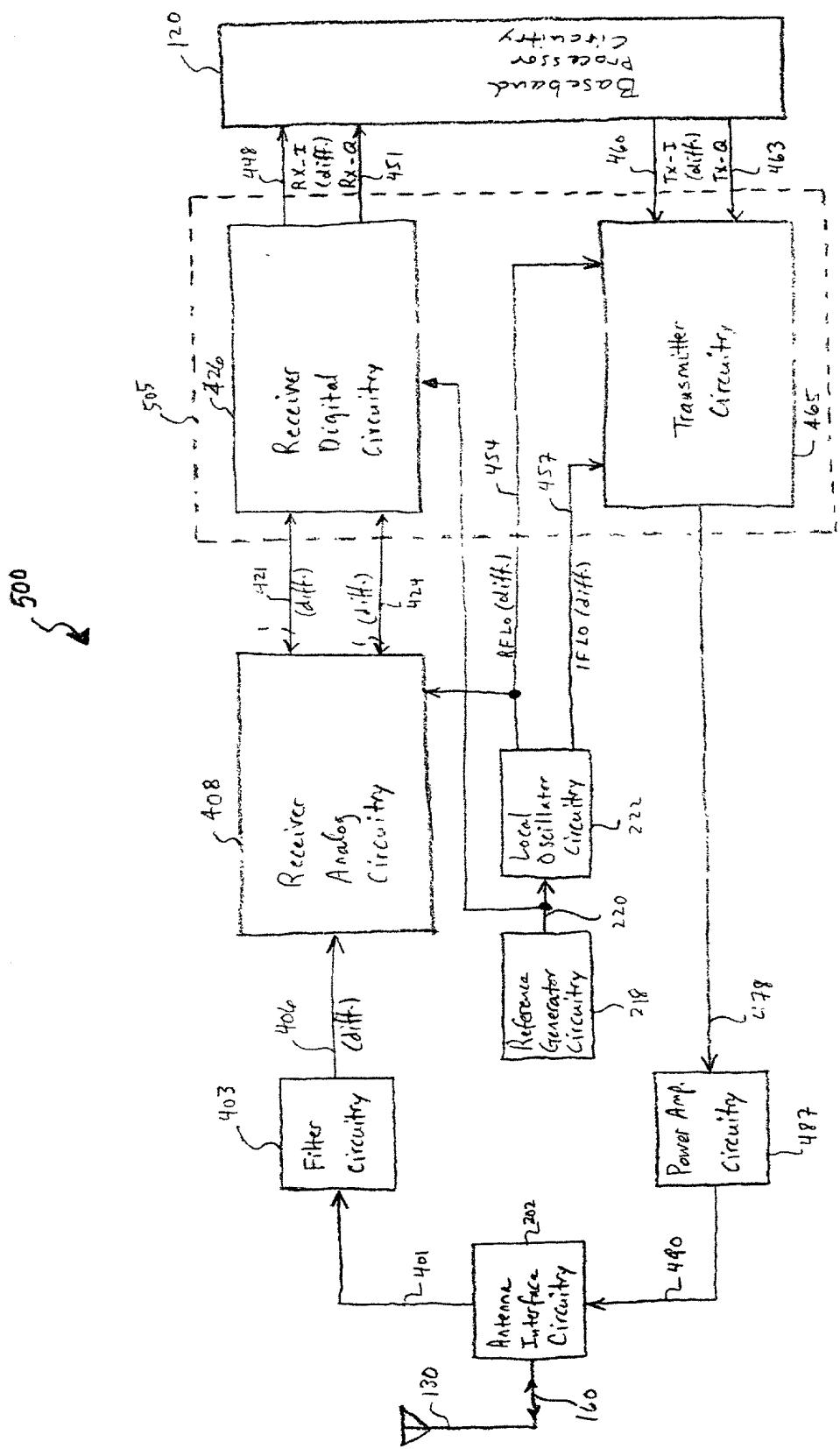


FIG. 5

600

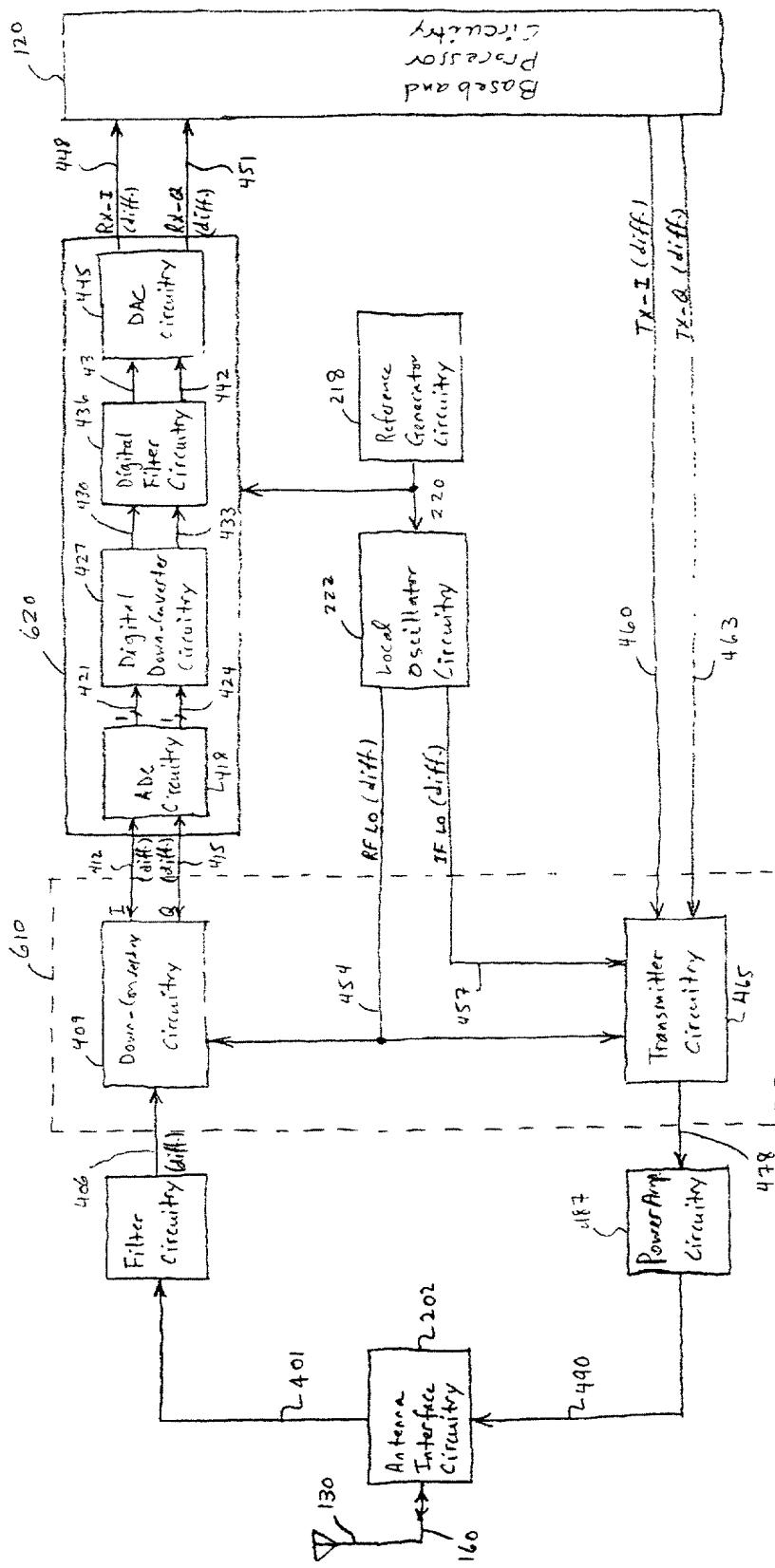


FIG. 6

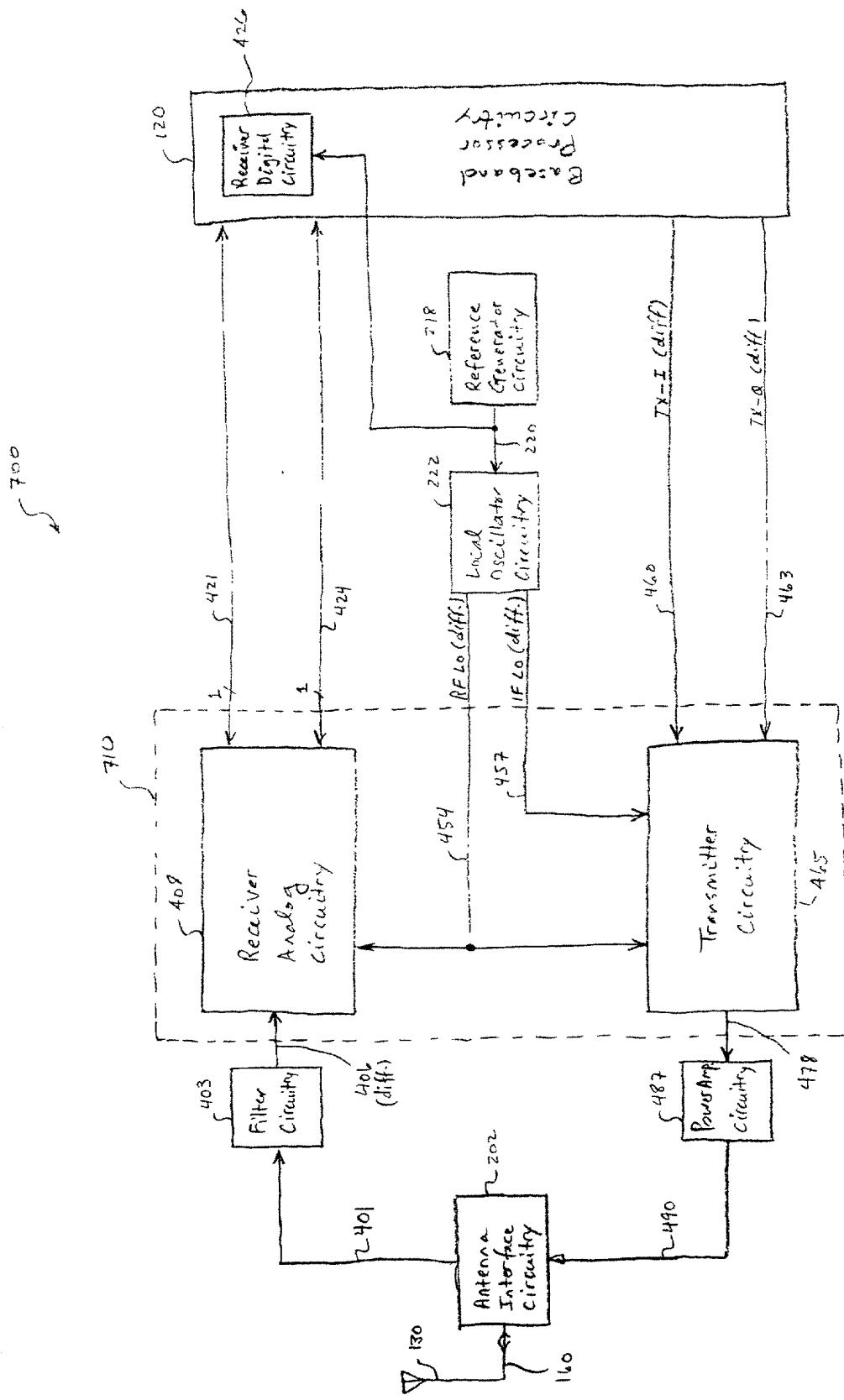


FIG. 7

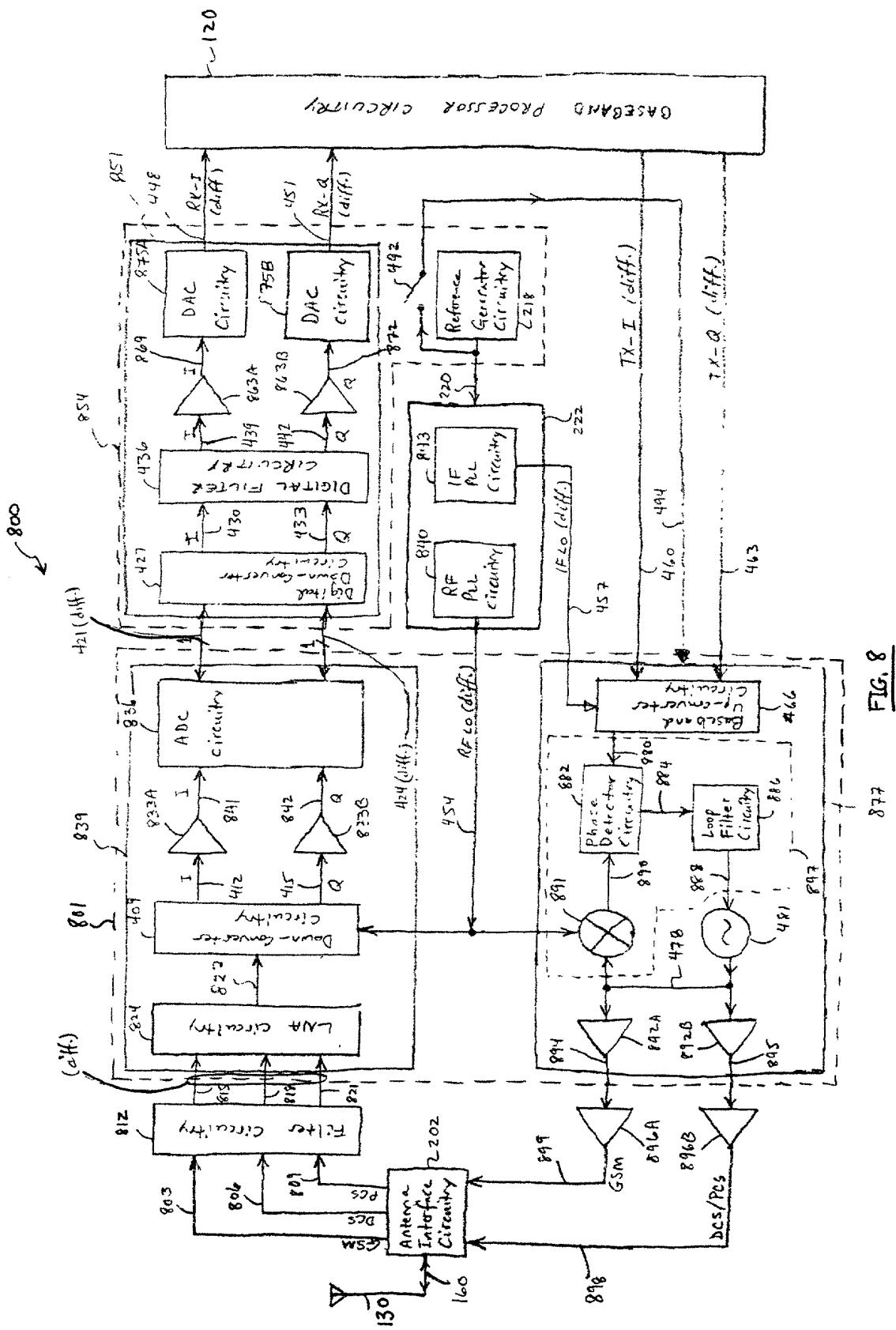


FIG. 8

900A

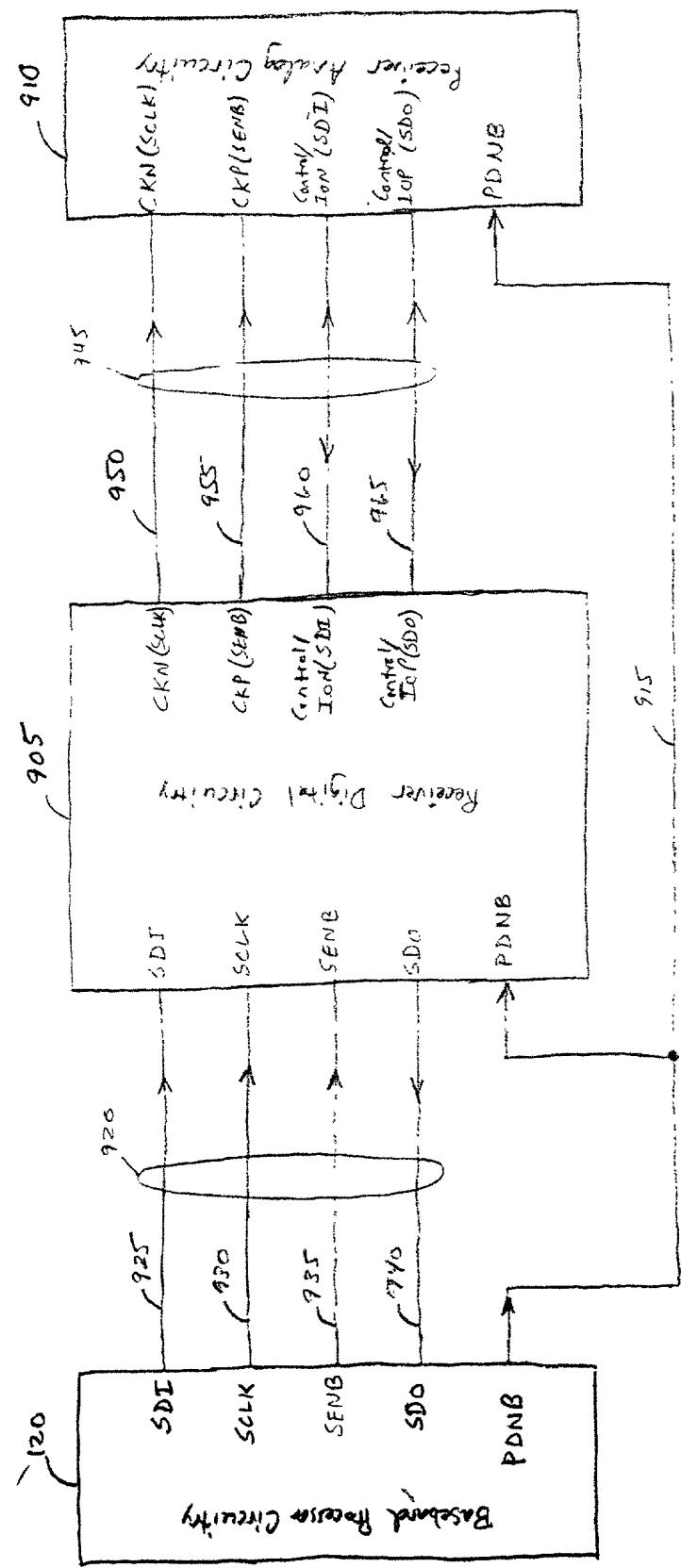


FIG. 9A

↙ 900B

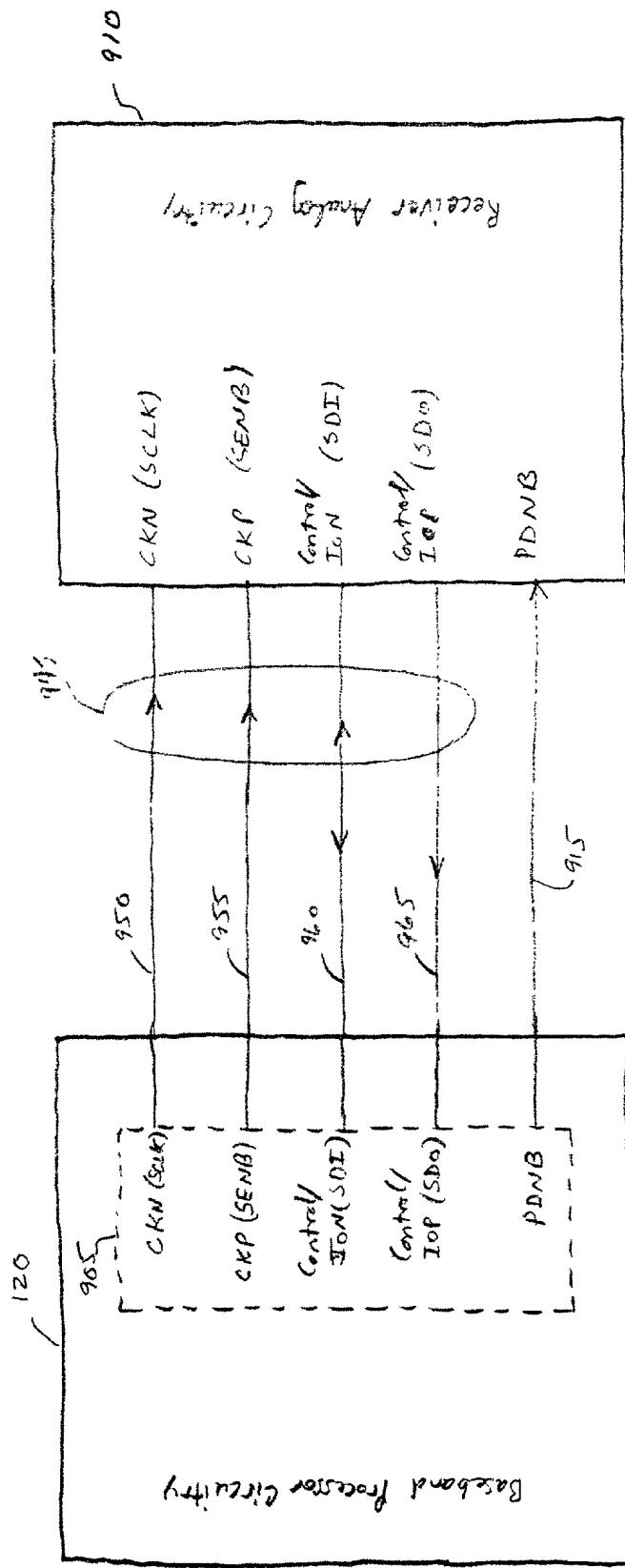


FIG. 9B

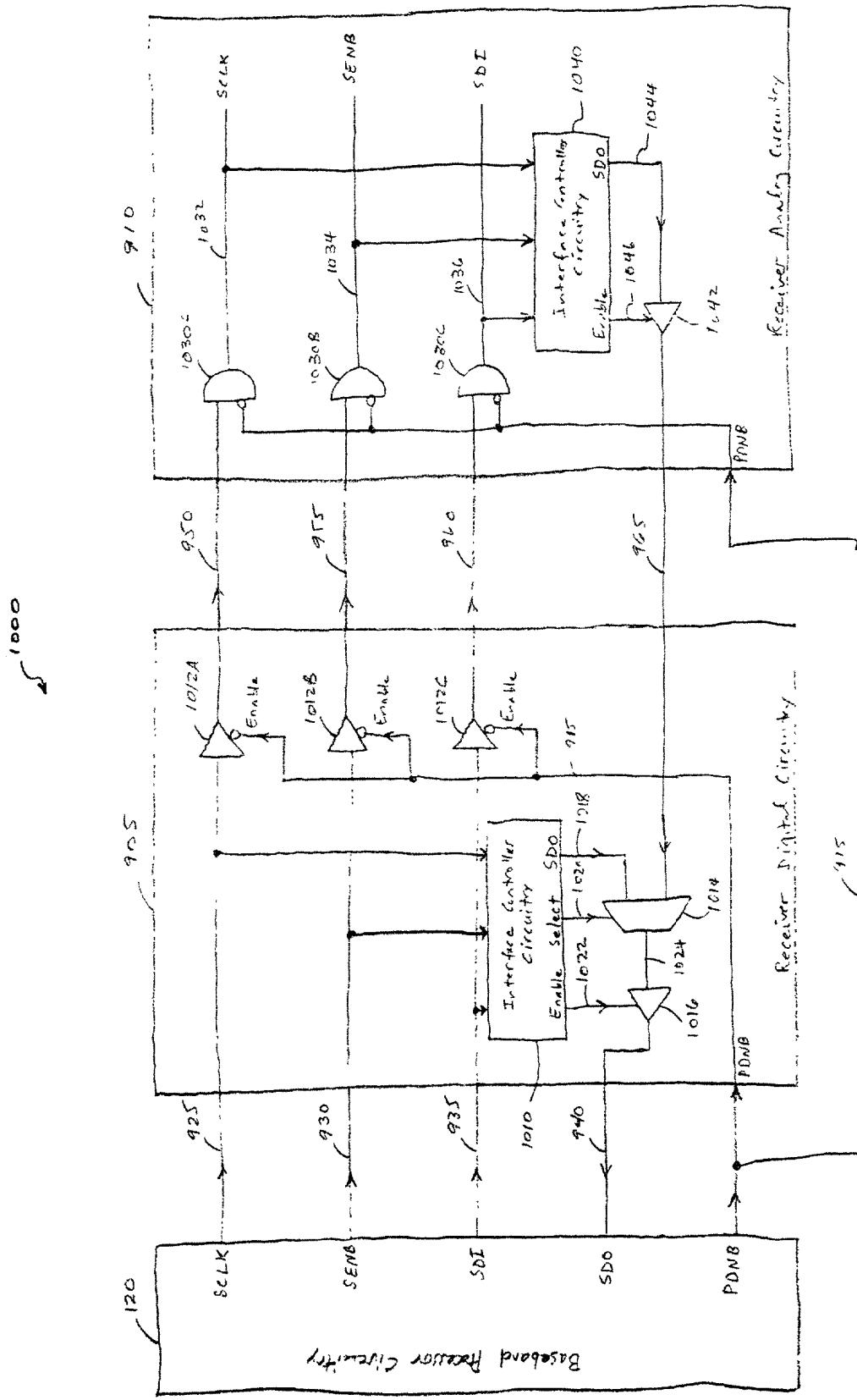
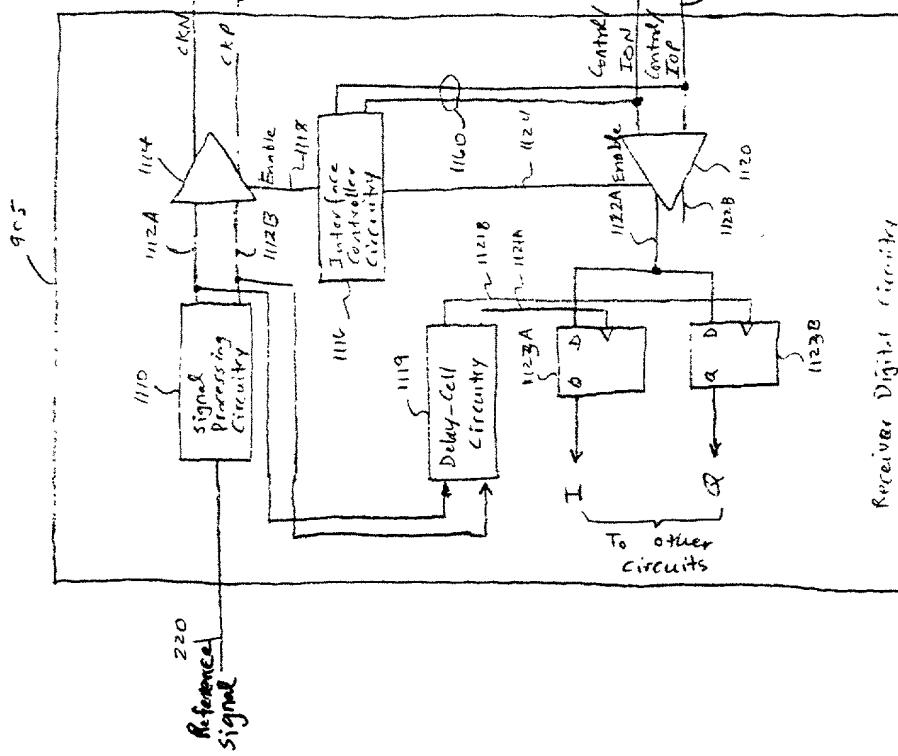
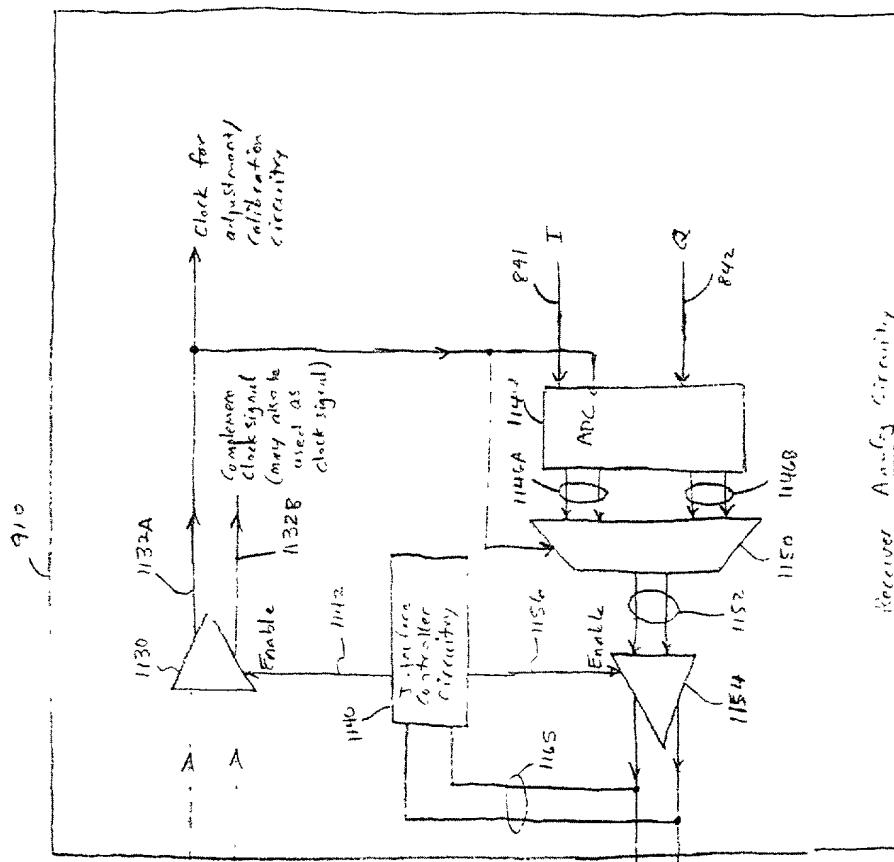


FIG. 10

1100A



Receiver Digital circuitry



Receiver Analog circuitry

FIG. 11A

11008  
S

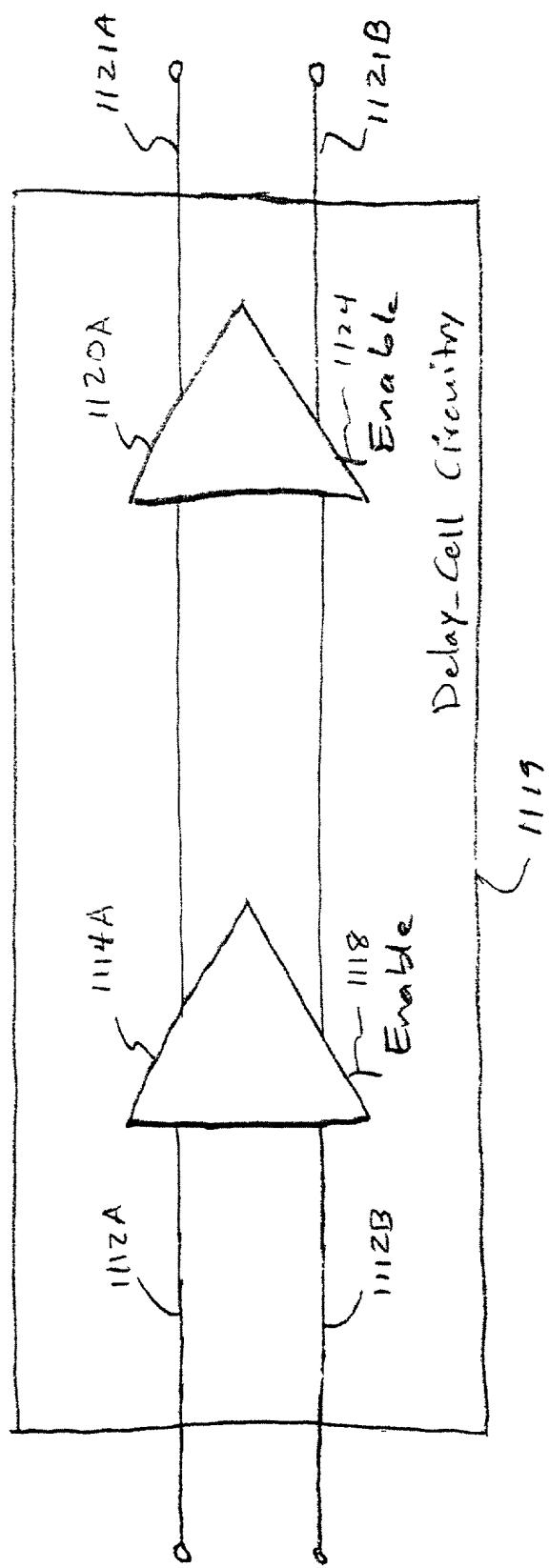


FIG. 11B

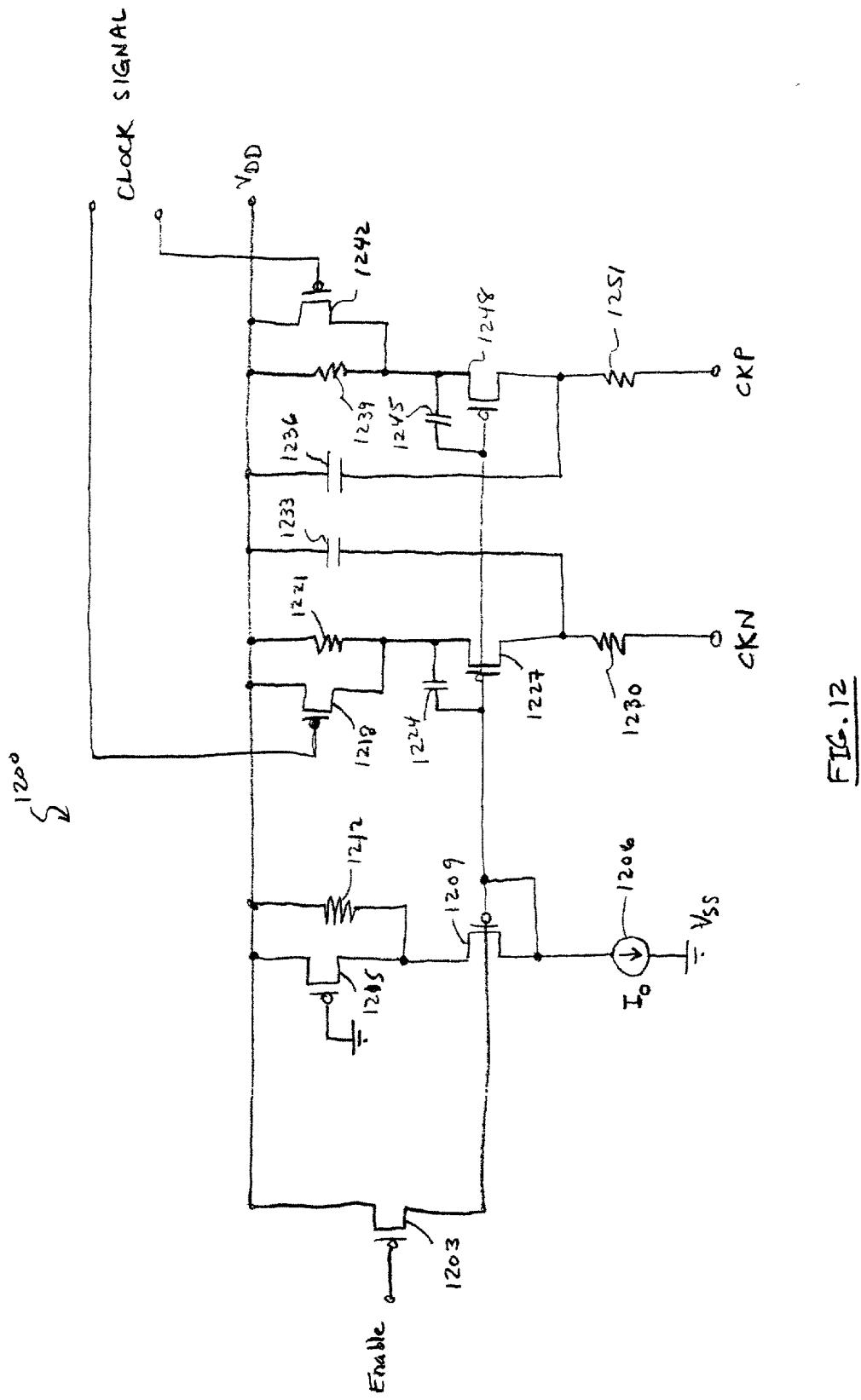


FIG. 12

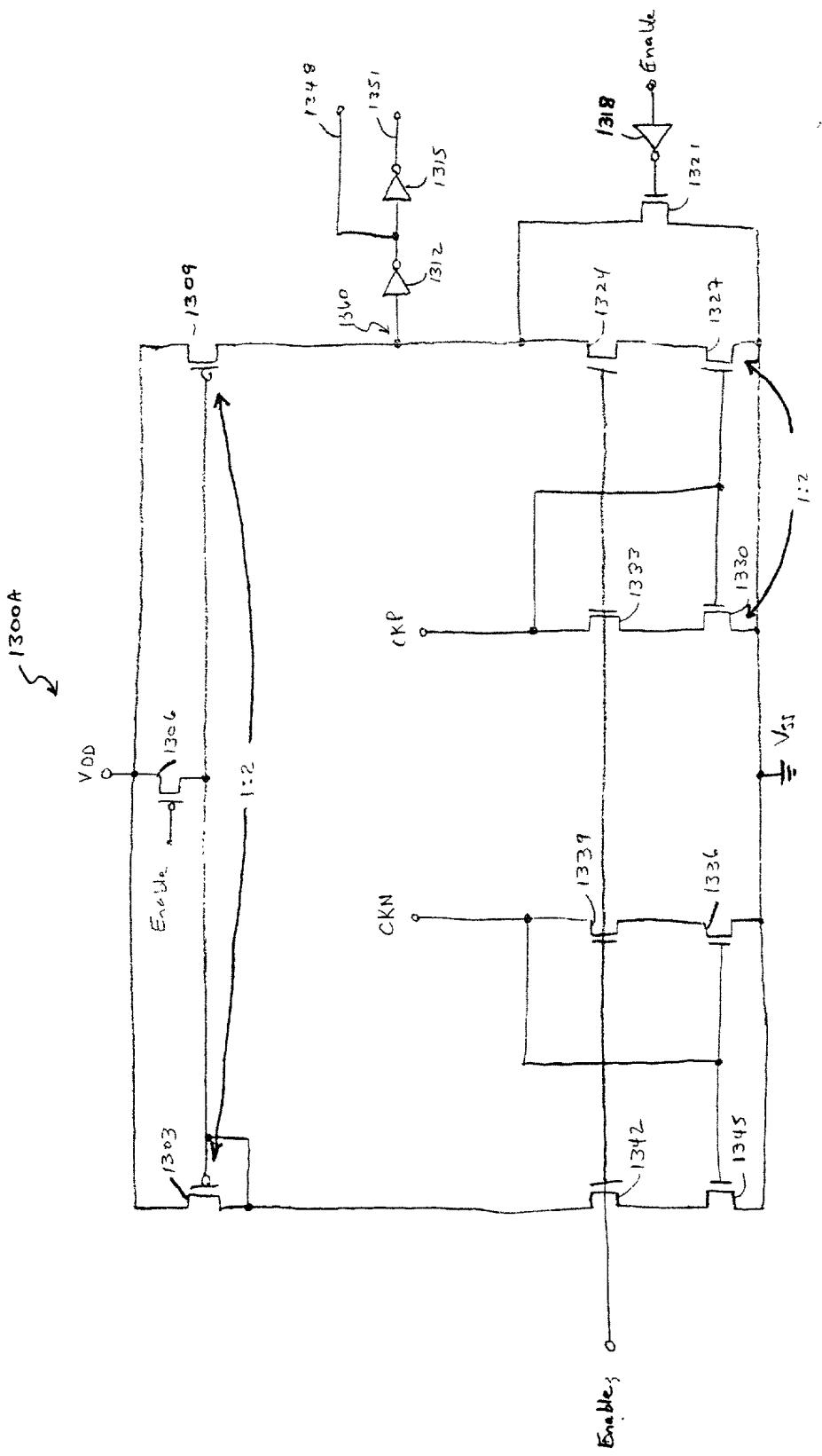


FIG. 13A

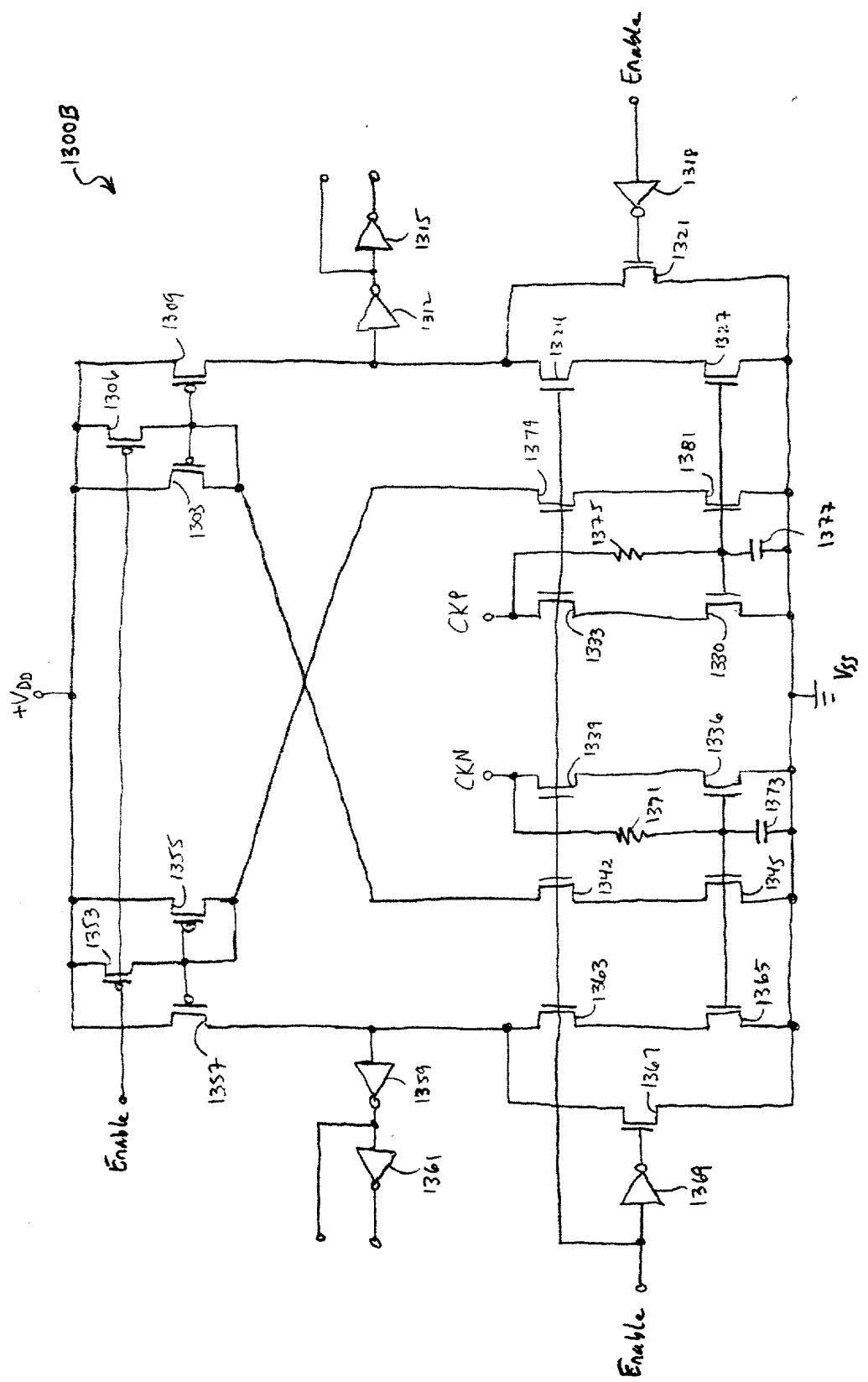
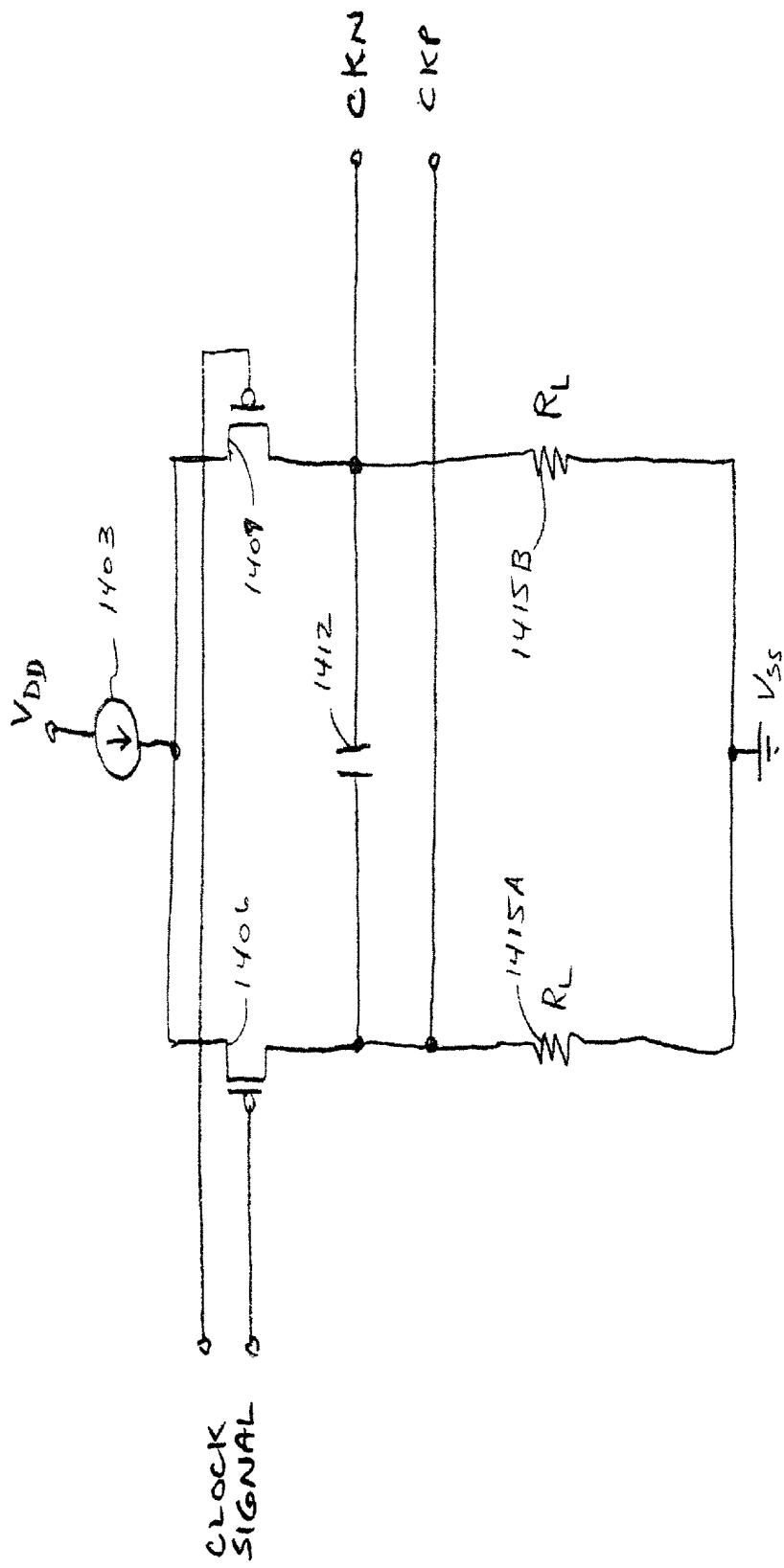


FIG. 13B

卷之三

1400 ✓



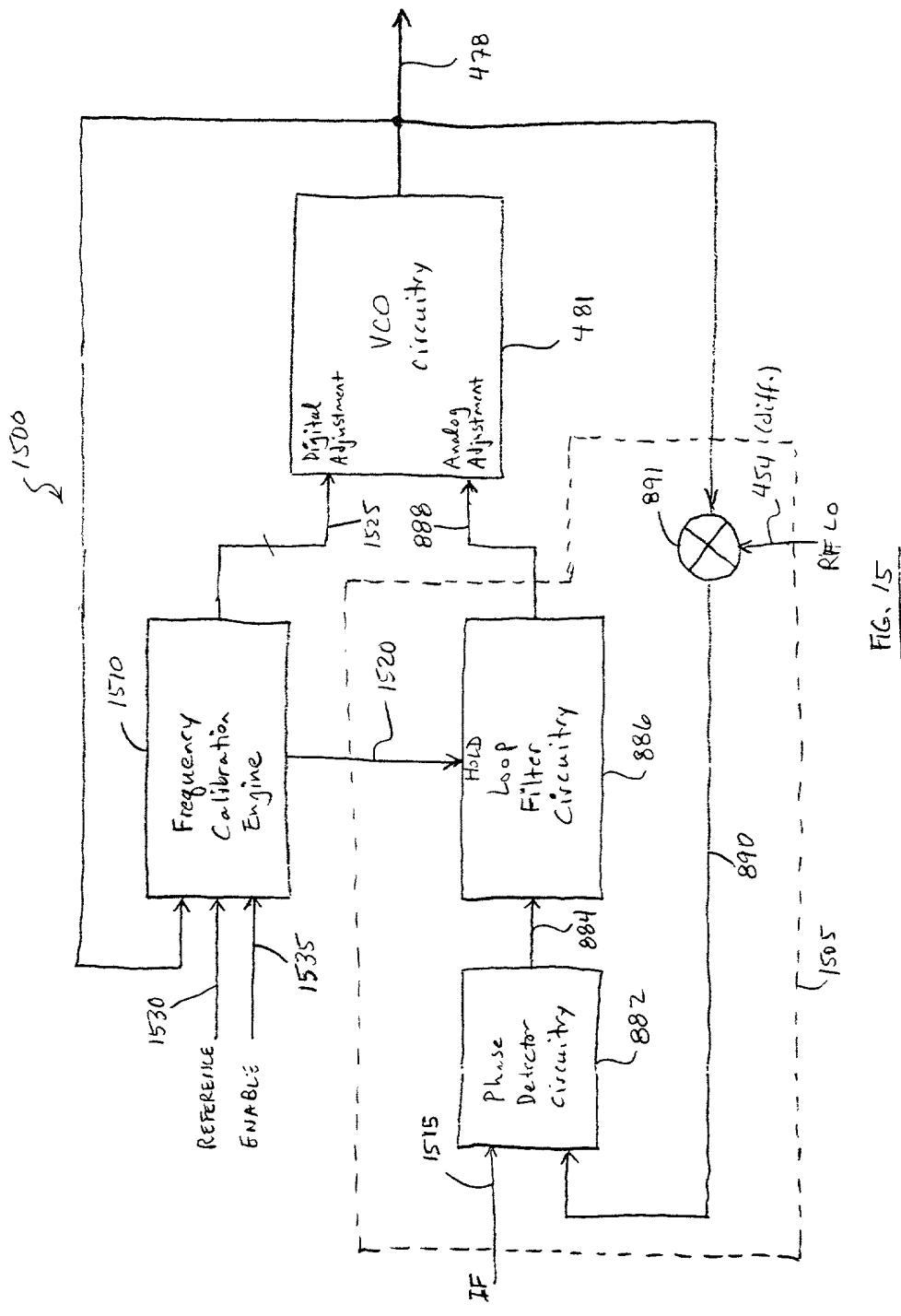


FIG. 15

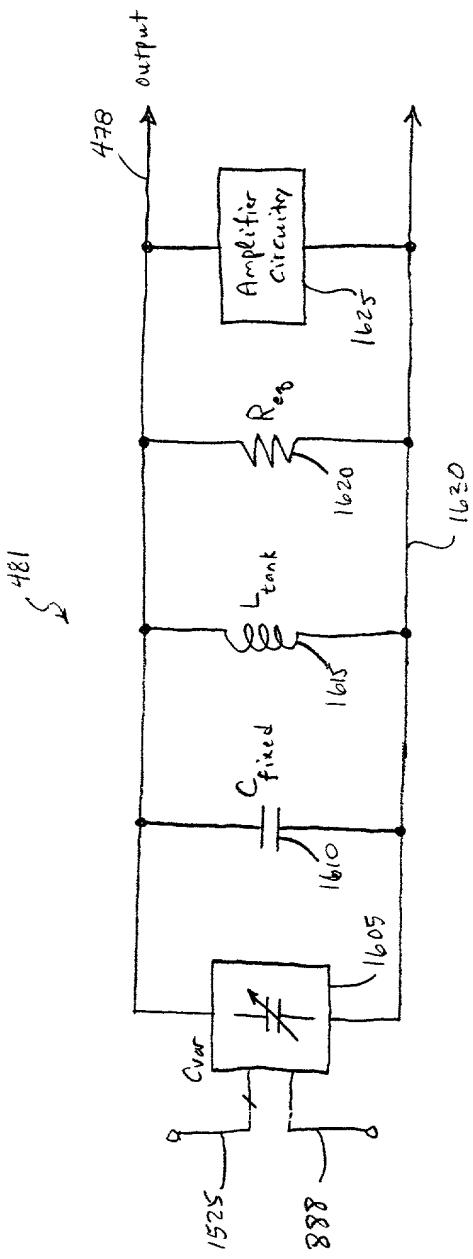


FIG. 16

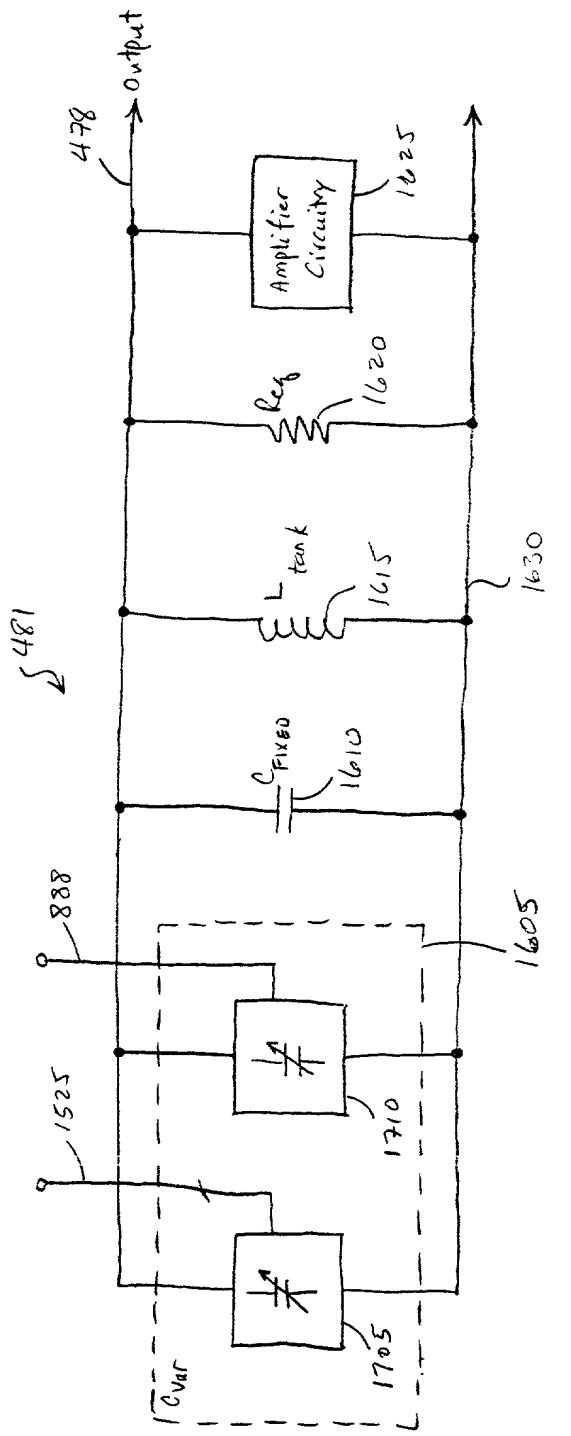


FIG. 17

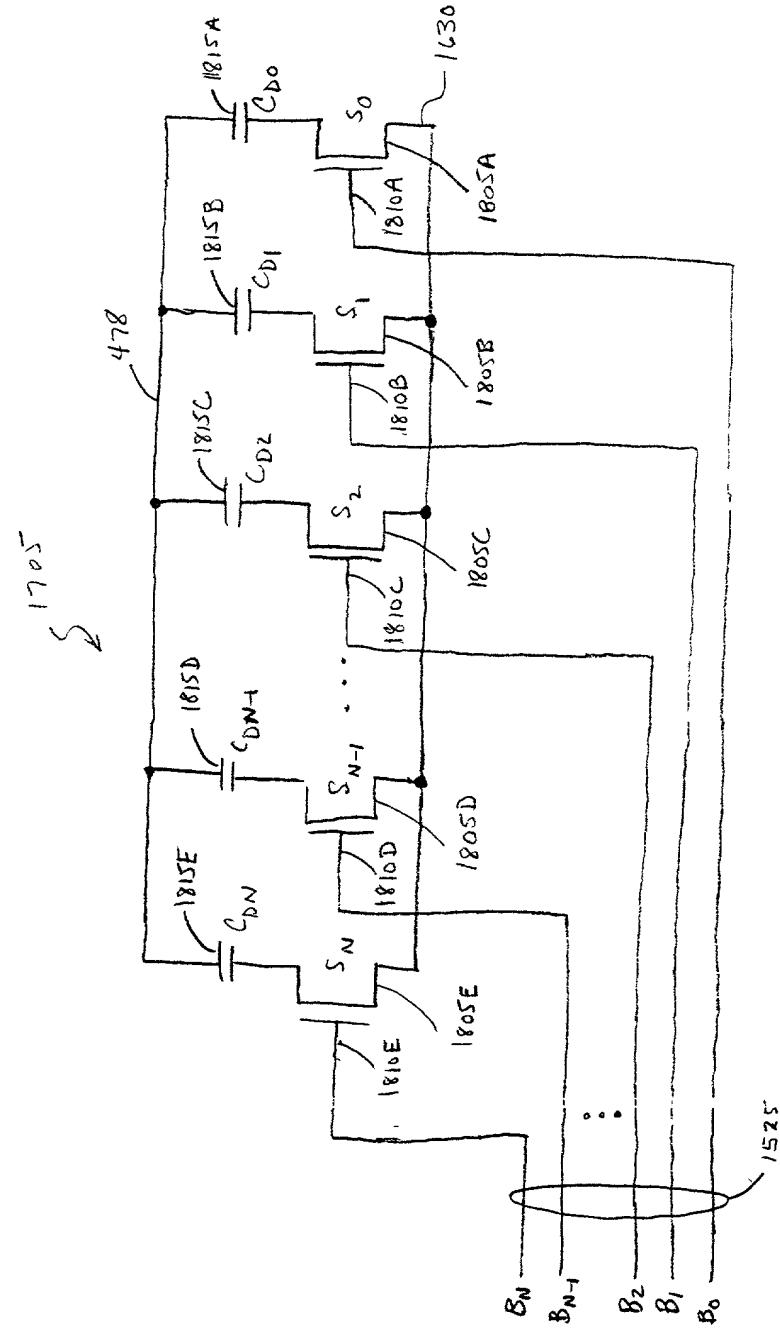


FIG. 18

1900A  
2

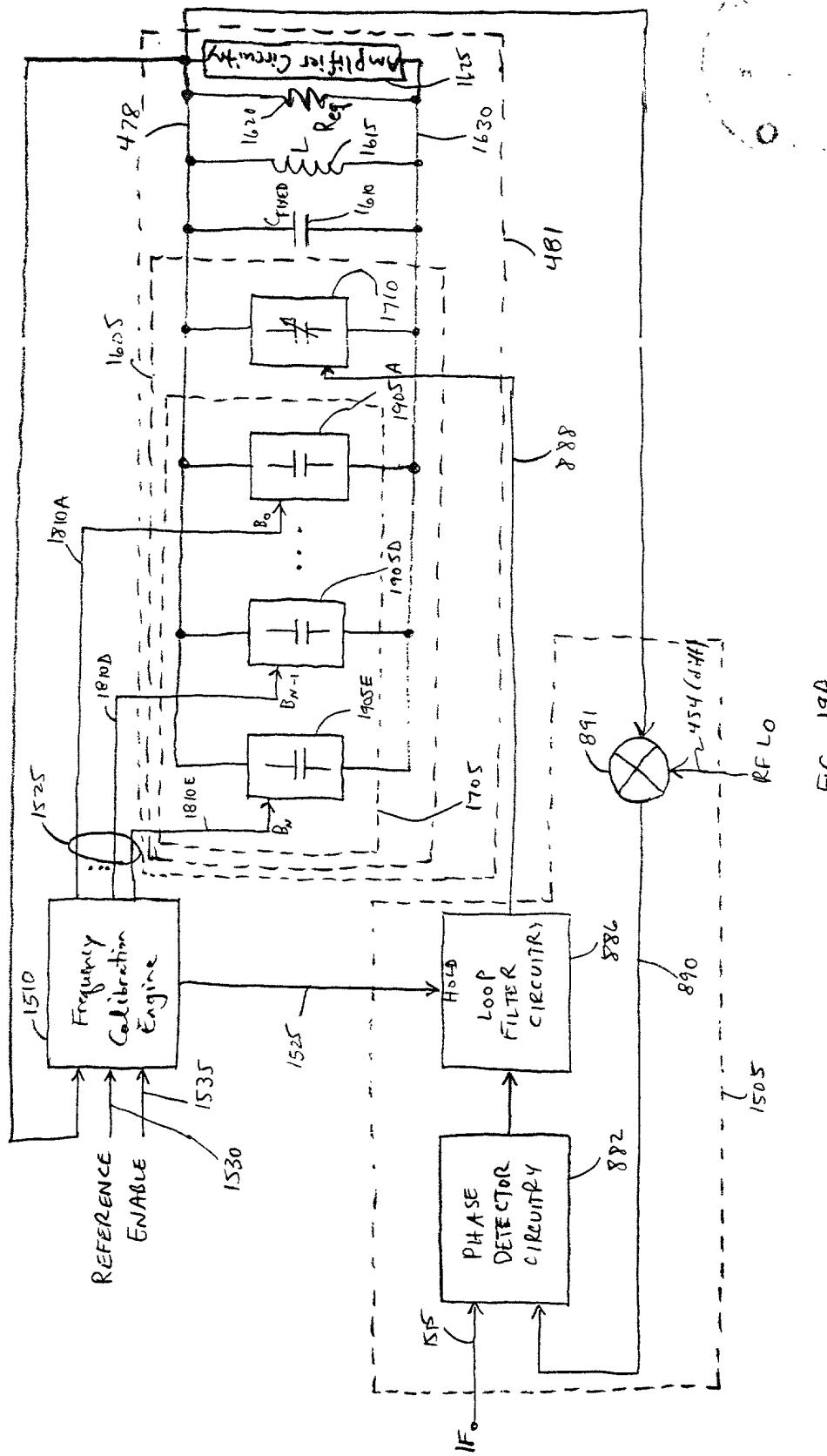


FIG. 19A

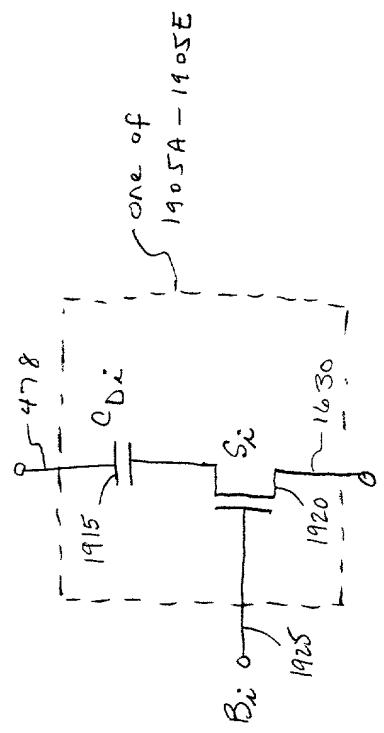


FIG. 19B

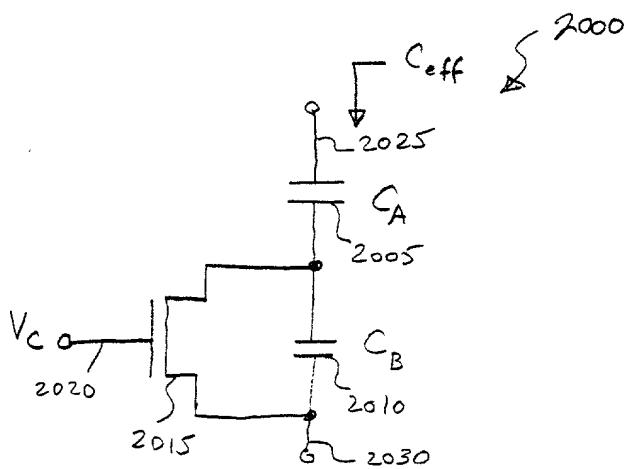


FIG. 20

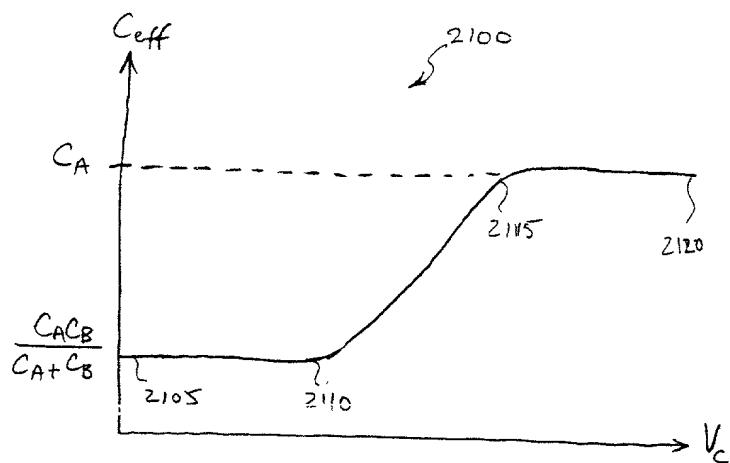


FIG. 21

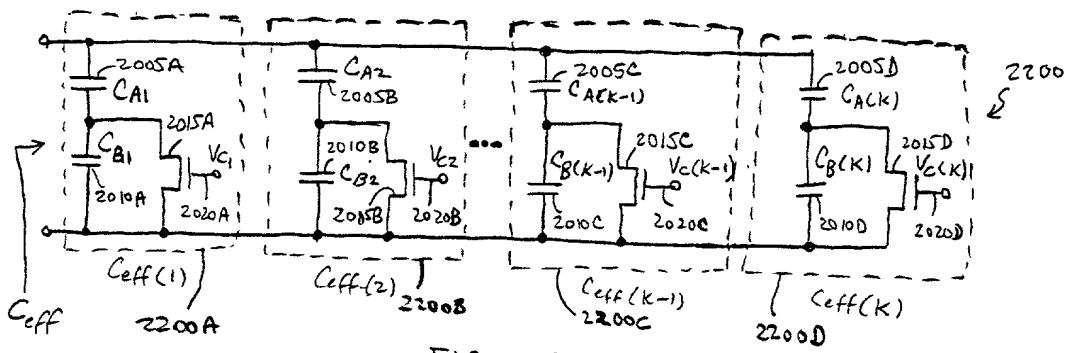


FIG. 22

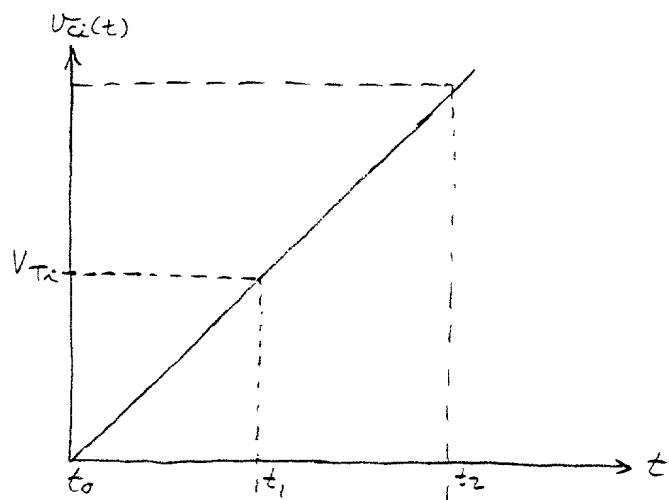


FIG. 23A

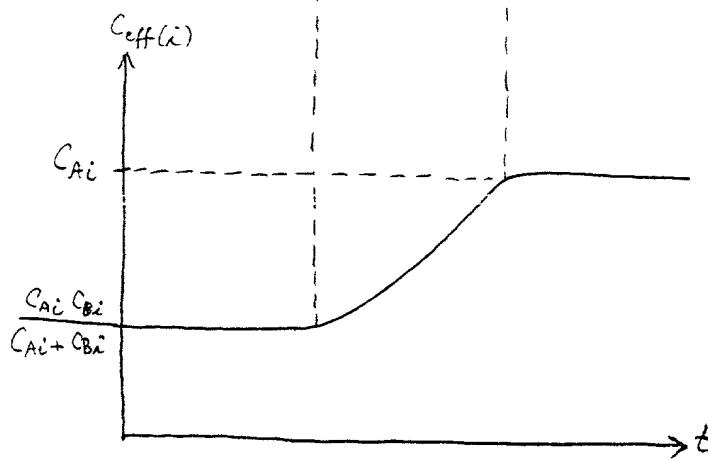


FIG. 23B

FIG. 24A

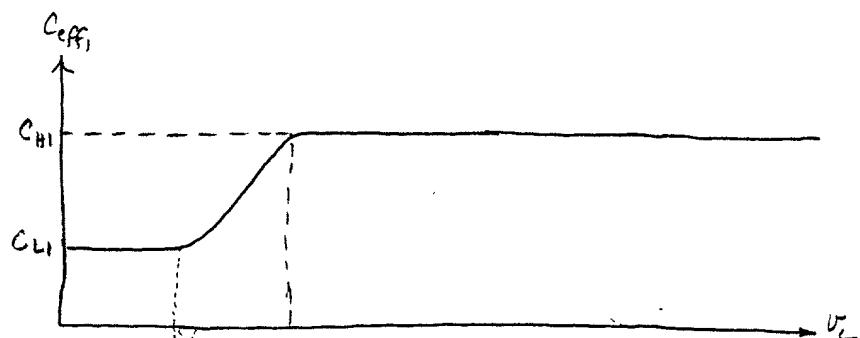


FIG. 24B

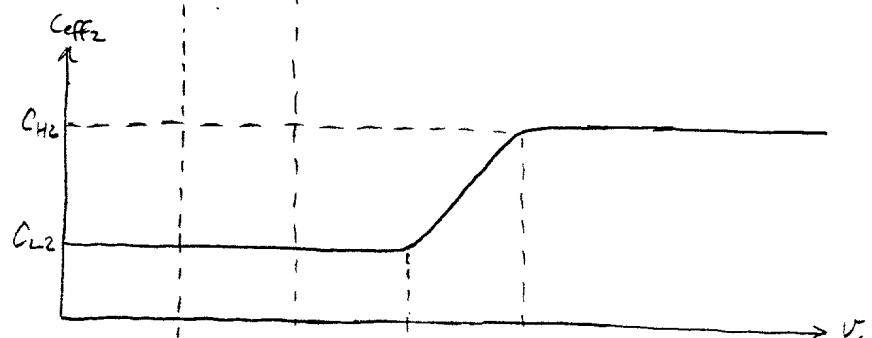


FIG. 24C

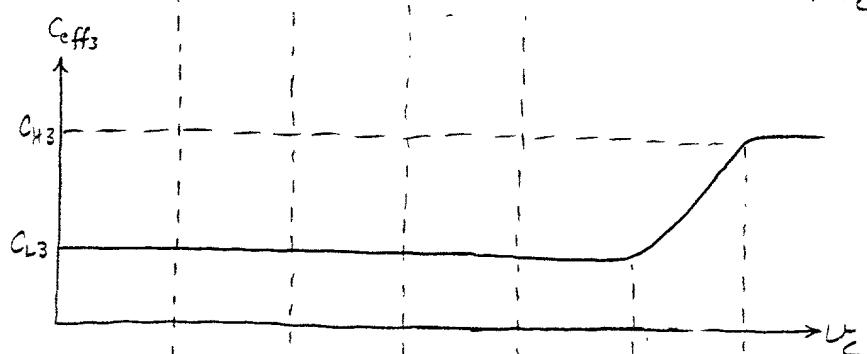
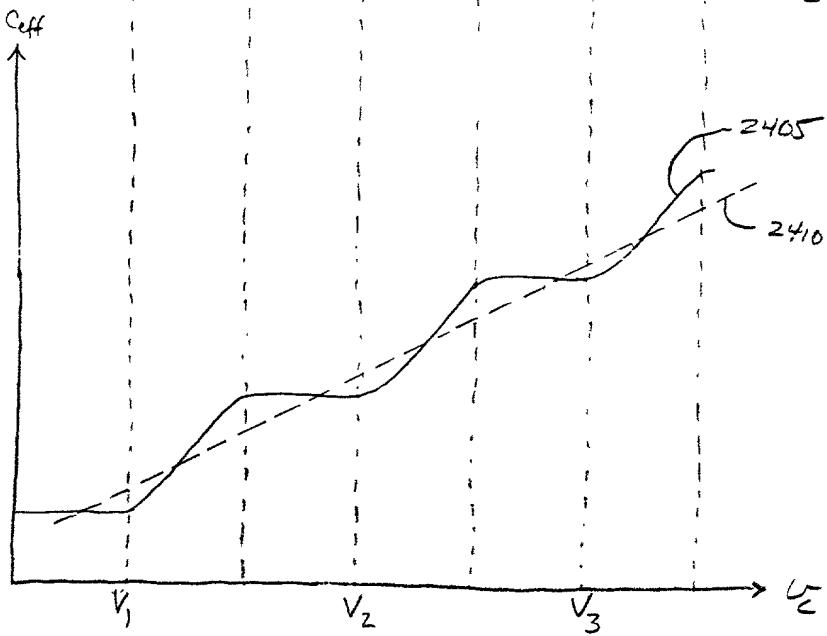


FIG. 24D



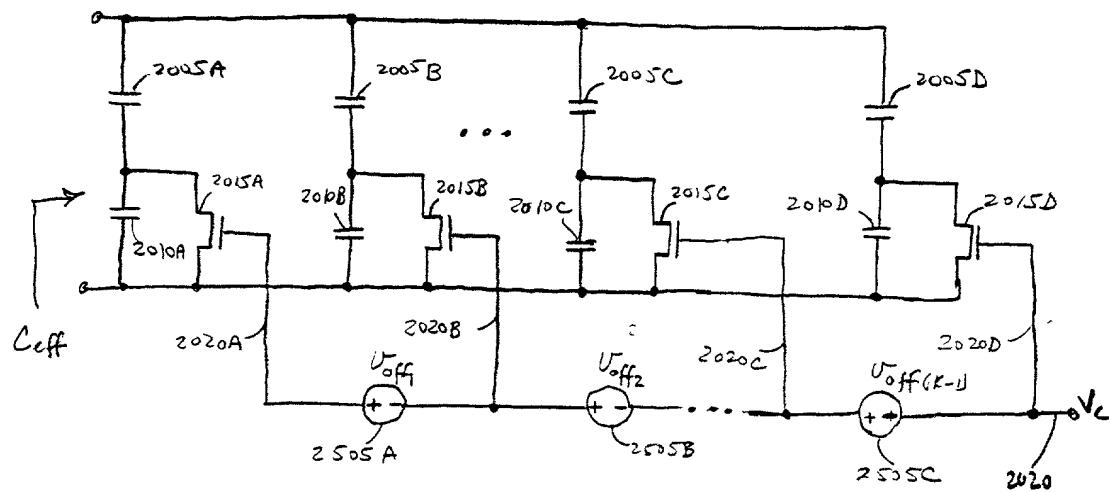


FIG. 25

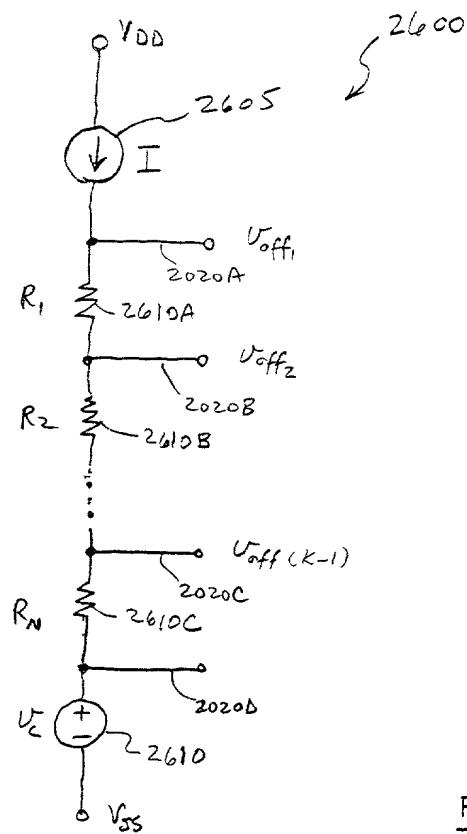


FIG. 26

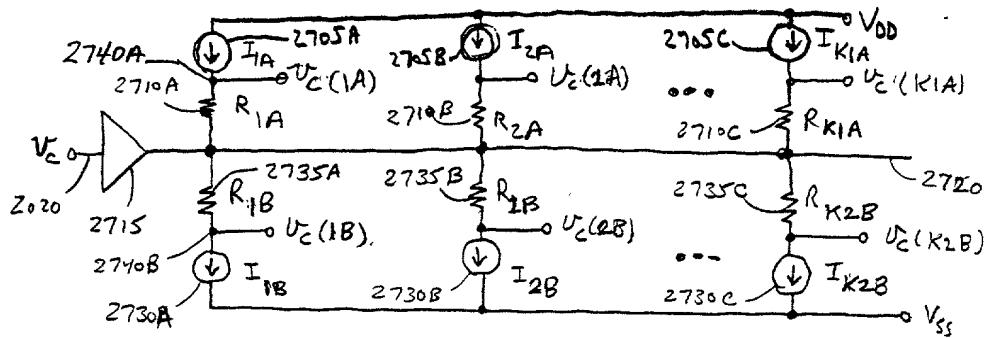


FIG. 27

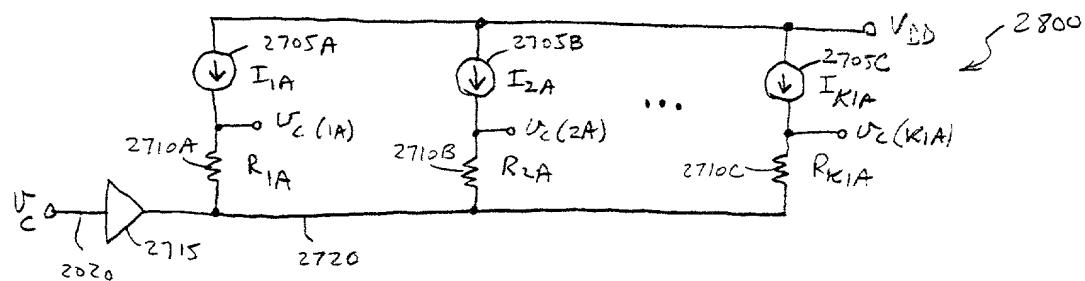


FIG. 28

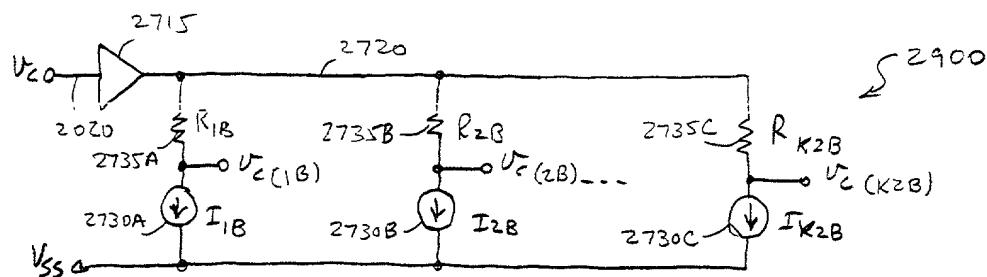


FIG. 29

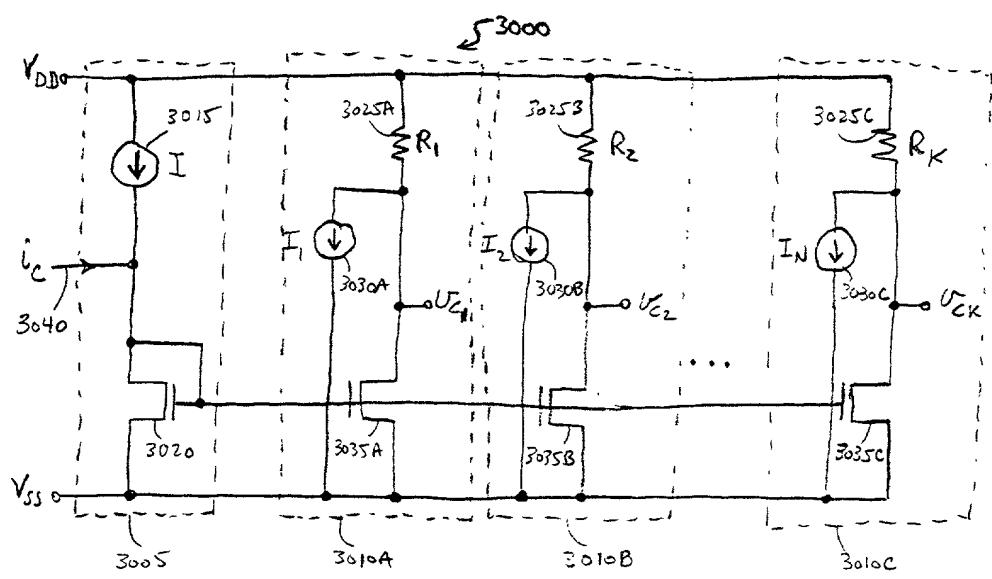


FIG. 30

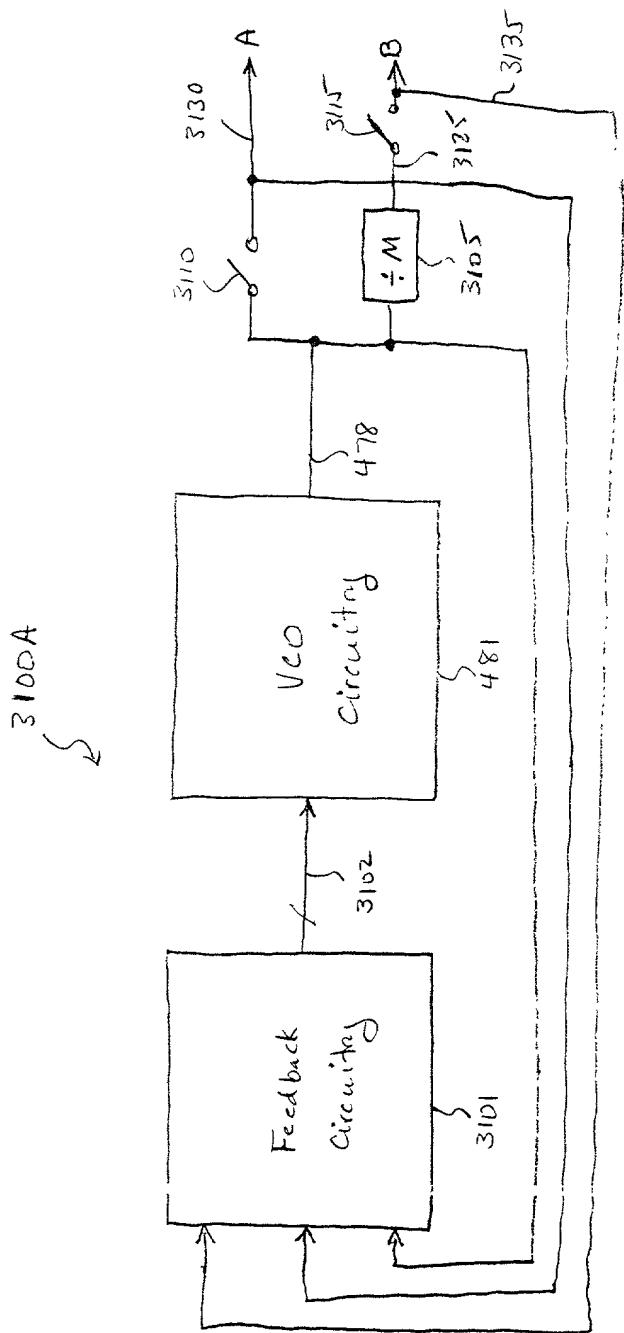
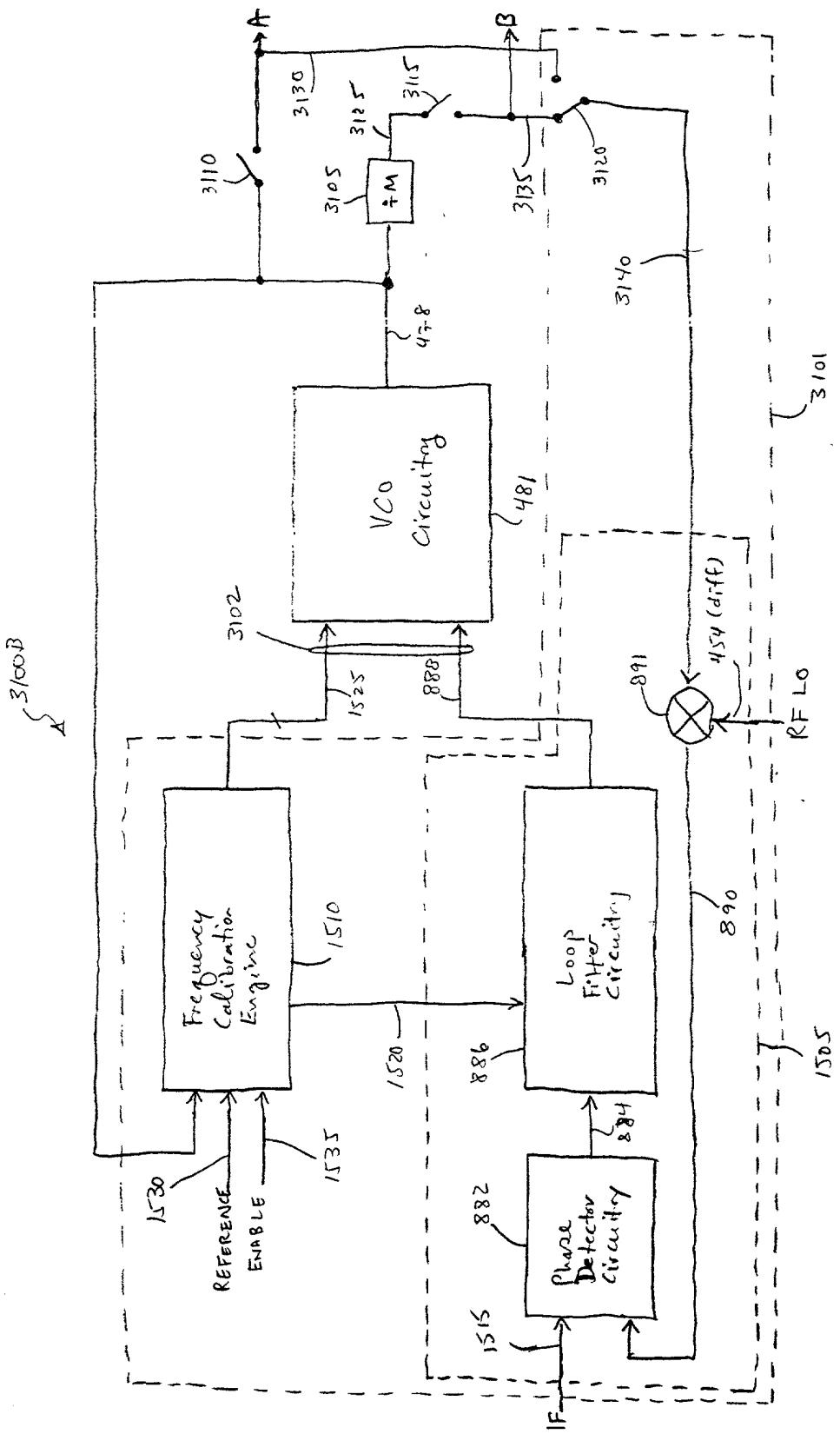


FIG. 31A



**FIG. 31B**

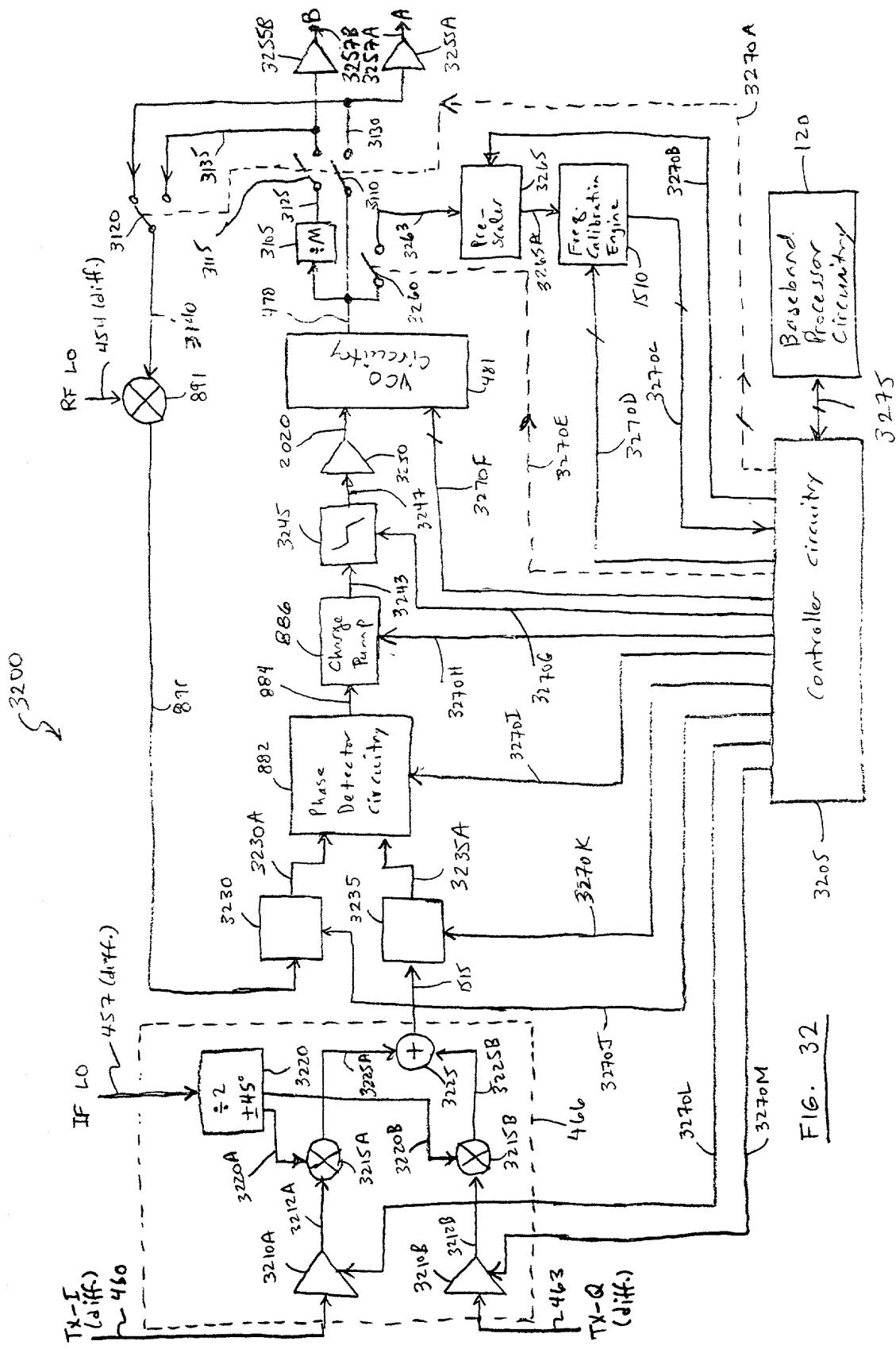


FIG. 32

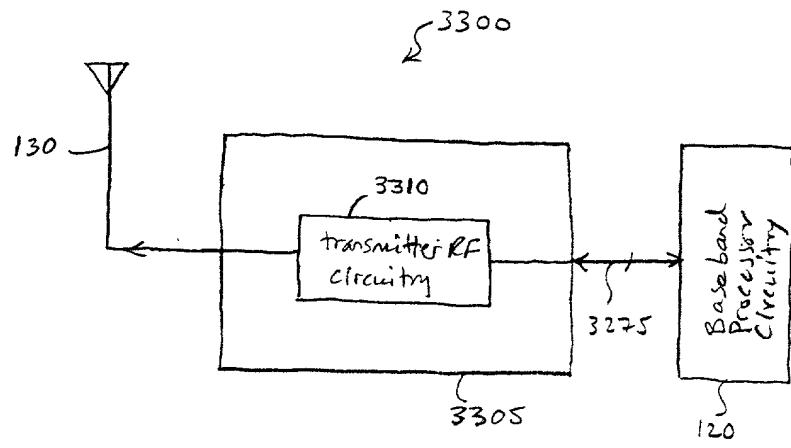


FIG. 33

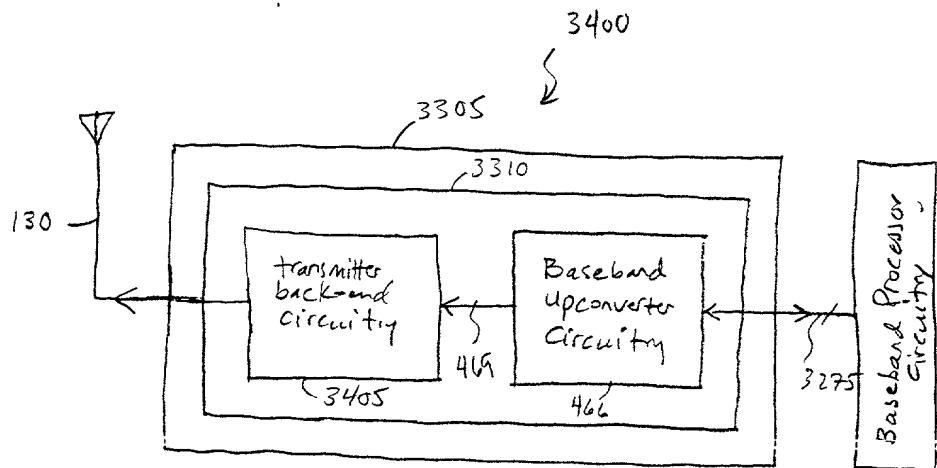


FIG. 34

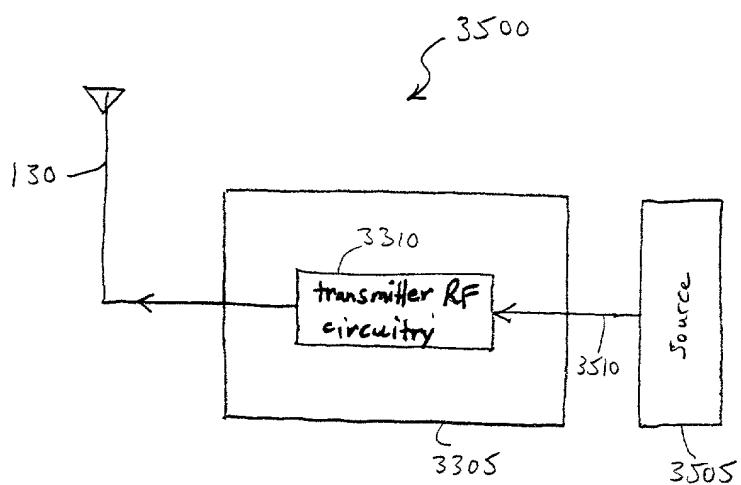


FIG. 35